

Wistron Confidential

MV-3

2008/08/20

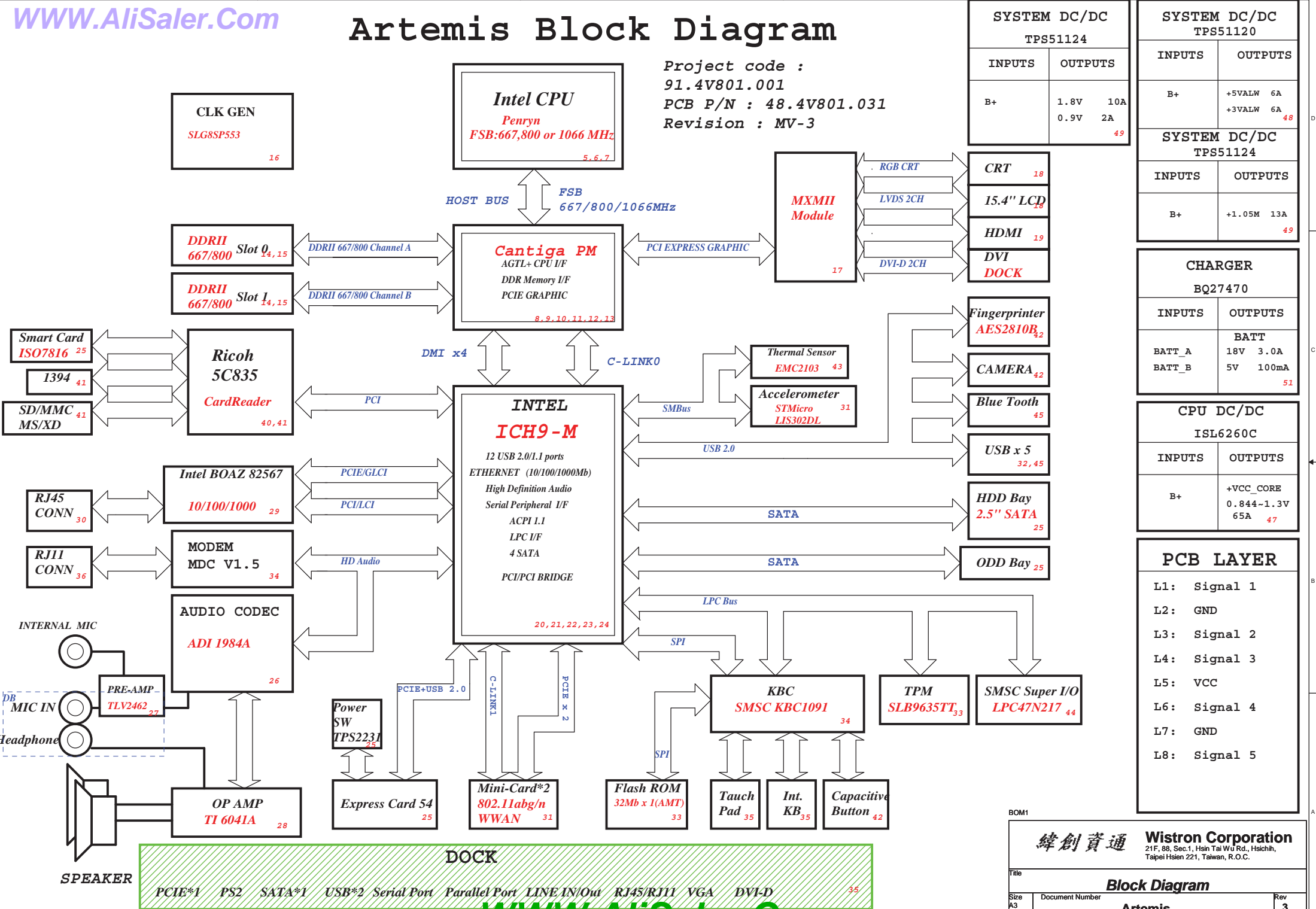
REV :MV-03

BOM1

緯創資通			Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.					
Title					
VOX					
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Artemis Block Diagram

Project code :
91.4V801.001
PCB P/N : 48.4V801.031
Revision : MV-3



BOM1

緯創資通

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File			Block Diagram	
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VOX		
<div><div>緯創資通</div><div>Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
Title		
Change Notes List		
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power plane State	+B +3VL LDO5	+5VALW +3VALW	+1.5V +5V +0.75V	+5VS +3VS +1.5VS +CPU_CORE +VCCP	+3VM +1.05VM	CLOCK
S0	O	O	O	O	O	O
S3/M1	O	O	O	X	O	O
S3	O	O	O	X	O	O
S5 S4/AC	O	O	X	X	O	O
S5 S4/Battery only	O	X	X	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X	X	X

PCI Devices

EETERNAL	IDSEL#	REQ/GNT#	PIRQ
Cardreader & 1394	AD22	2	G,E

DMA Channel	Device
DMA0	Modem/LAN
DMA1	ECP
DMA2	Floppy Disk
DMA3	Audio
DMA4	(Cascade)
DMA5	Unused
DMA6	Unused
DMA7	Unused

USB PORT#	Destination
0	USB1
1	USB4
2	EXPRESS SLOT
3	USB5
4	USB2
5	USB3
6	Bluetooth
7	WWAN
8	Fingerprint
9	Dock 1
10	Camera
11	Dock 2

Symbols	Description
DY/DUMMY	No install
1KR2J	Resistor 1K ohm ,Size 0402 ,5%
1KR3F	Resistor 1K ohm ,Size 0603 ,1%
GP	ROHS parts
NC	Pin no connect to anything

IRQ	Device
0	System Timer
1	Keyboard
2	N/A
3	Serial port (COM2) ,LAN/Modem
4	Serial port (COM1)
5	Audio/VGA
6	Floppy
7	Parallel port
8	System CMOS/Real-time clock
9	Microsoft ACPI
10	N/A,Modem,LAN
11	Mass storage control/PCI simple communication control
12	synactic PS2 port GlidePAD
13	Numeric Data Process
14	Primary IDE interface ,HDD
15	Secondary IDE interface ,CD-ROM
16	Mobile Intel Crestline Express Chipset Family Microsoft UAA Bus Drive for High Definition Audio Intel 82801H (ICH8 Family) PCI Express Root Port -27D0
17	Intel 82801I (ICH9 Family) PCI Express Root Port -27D2 Intel 82801I (ICH9 Family) USB Universal Host Control
18	Intel 82801I (ICH9 Family) USB Universal Host Control Richo R5C835 Integrates FlashMedia Control Richo R5C835 Gemcore based SmartCard Control
19	Intel 82801I (ICH9 Family) PCI Express Root Port -27D6 Intel 82801I (ICH9 Family) USB Universal Host Control
20	Intel 82801I (ICH9 Family) USB Universal Host Control Intel 82801I (ICH9 Family) USB2 Enhanced Host Control
21	Intel 82801I (ICH9 Family) USB Universal Host Control
22	SDA Standard Compliant SD Host Control Accelerometer LIS302DL
23	HP Mobile Data Protection Sensor

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Title

Artemis List

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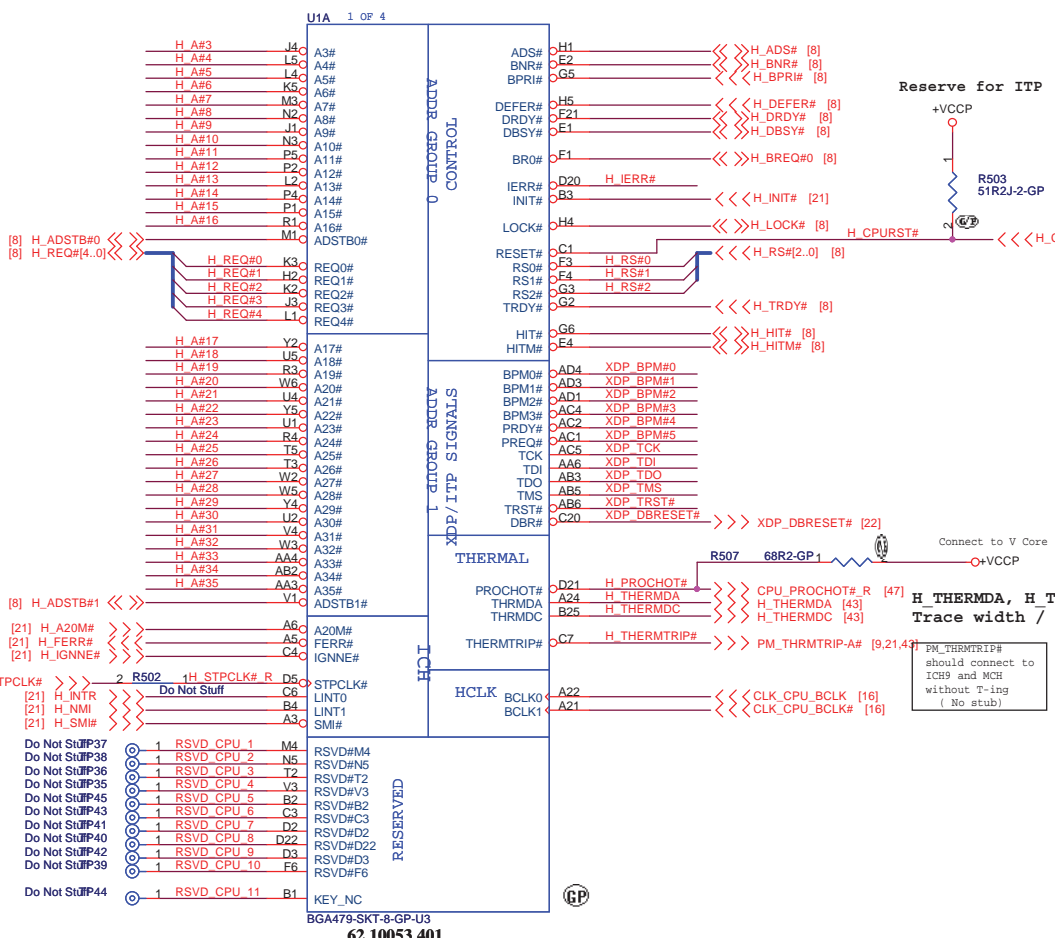
D

C

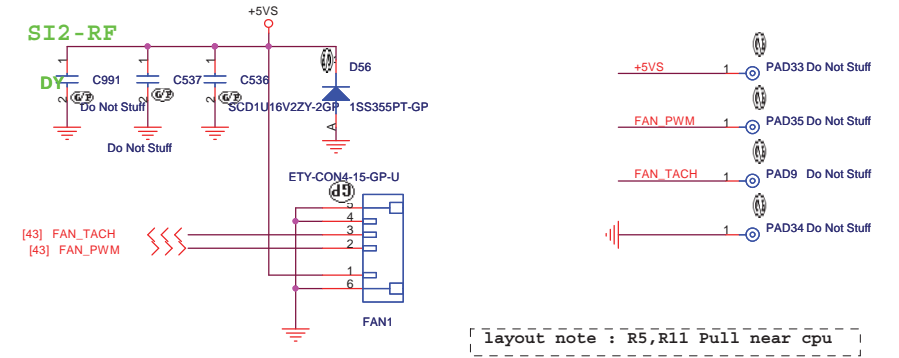
TEST7

B

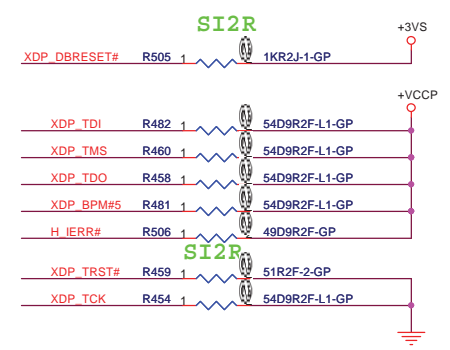
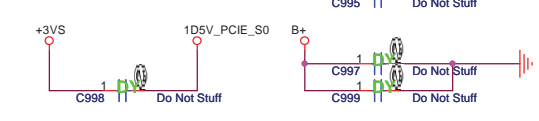
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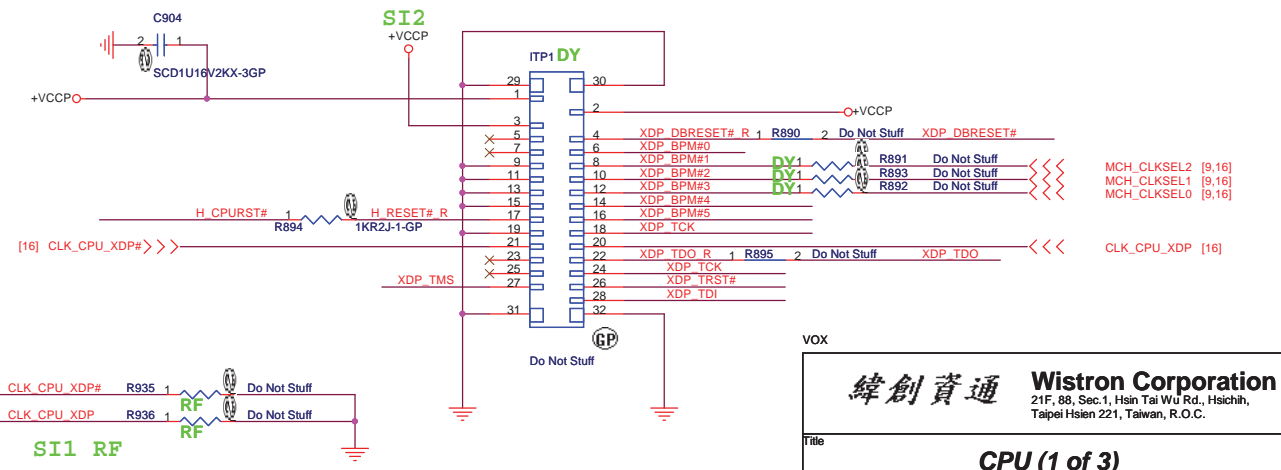
4 WIRE PWM Fan Control circuit



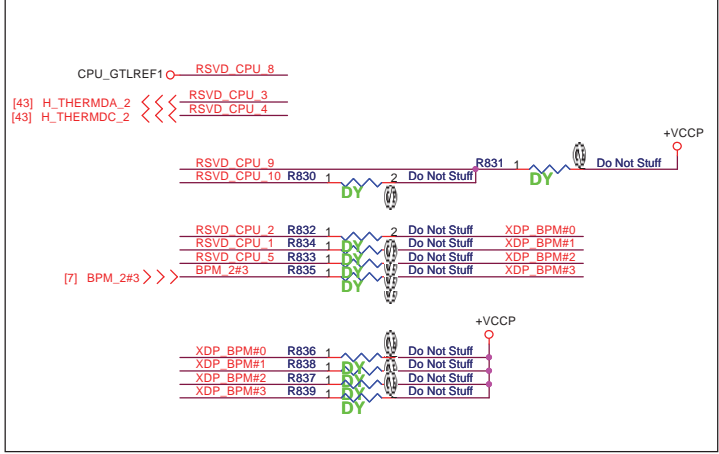
SI2-RF for CPUCLK



DB3



DB2 Quad Core support



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CPU (1 of 3)

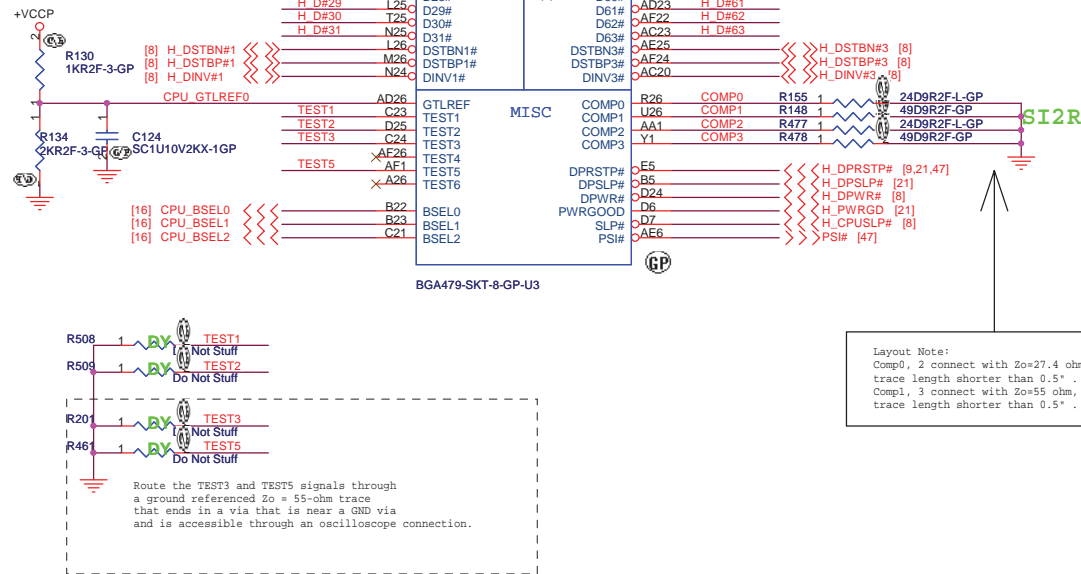
Artemis

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CPU (2 of 3)

Layout notes
Z= 55 Ohm 0.5" MAX for GTLREF



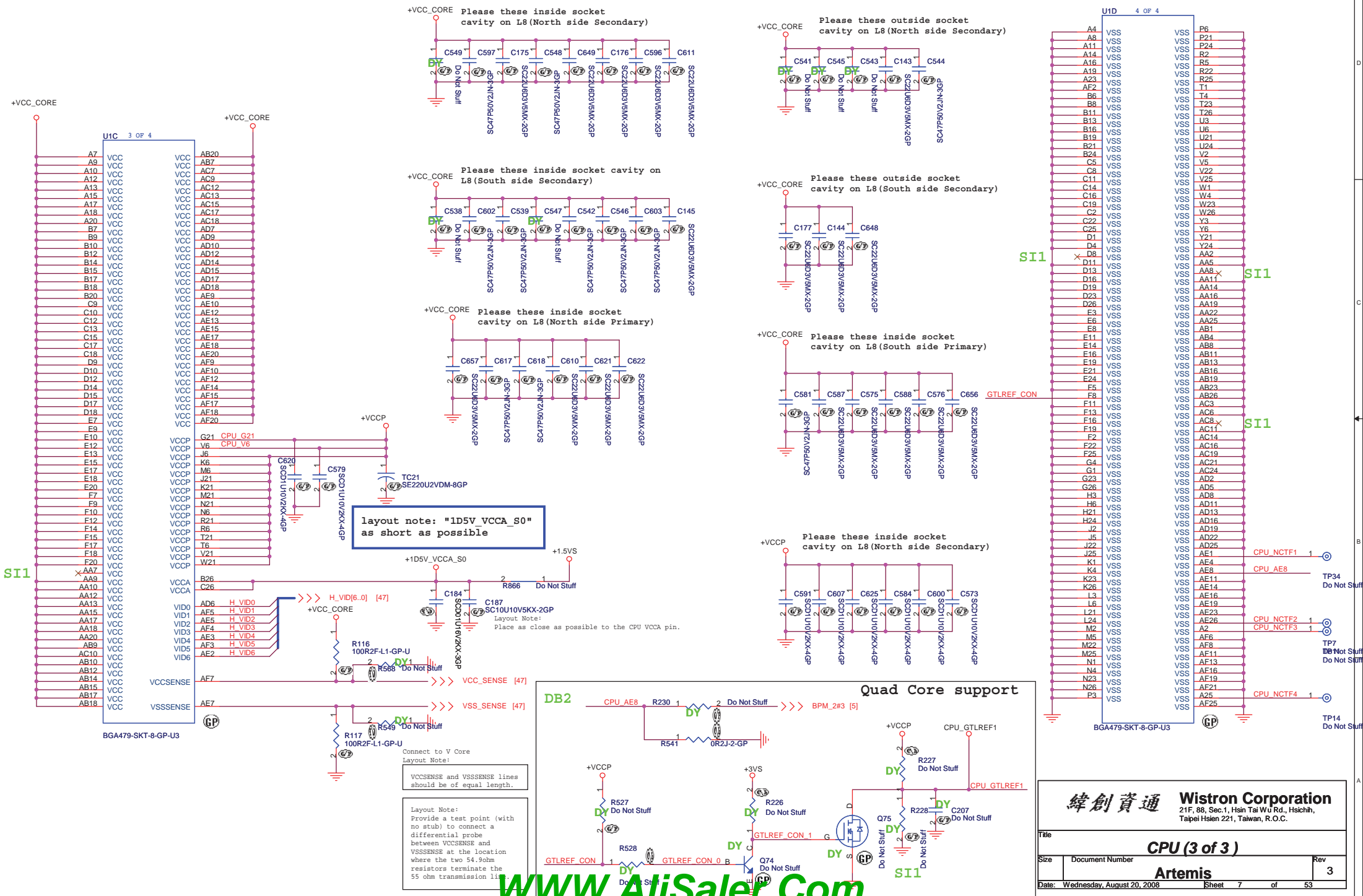
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CPU (2 of 3)

Artemis

3

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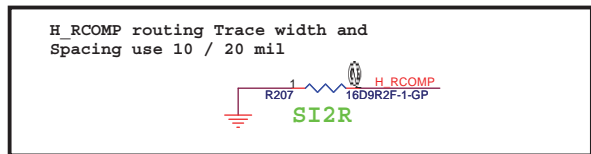
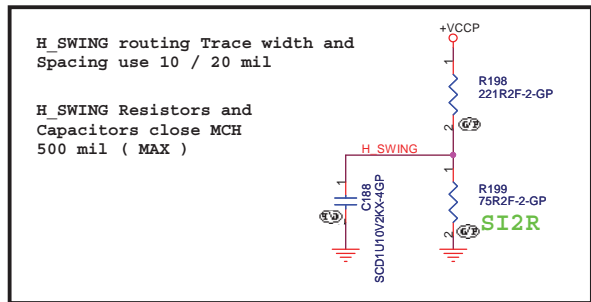


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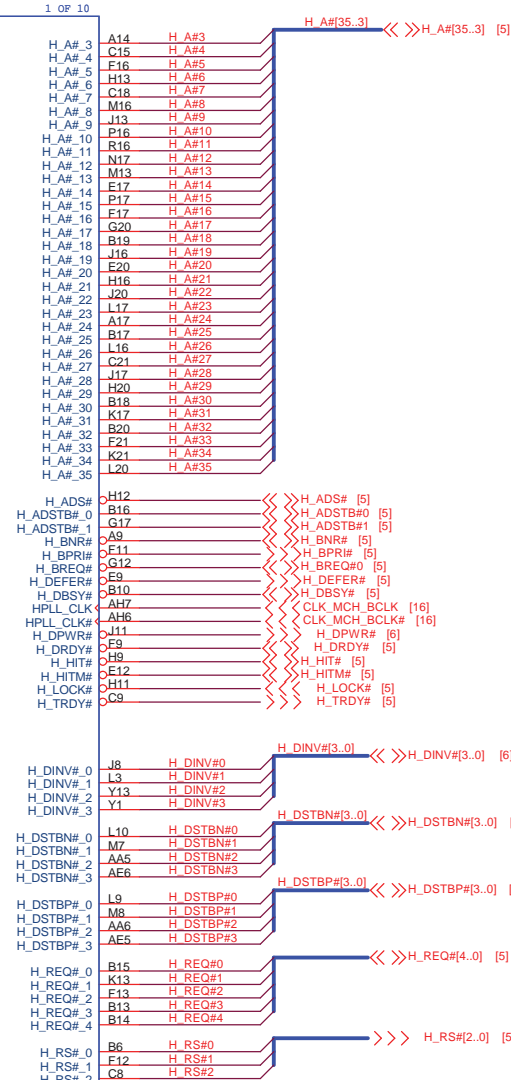
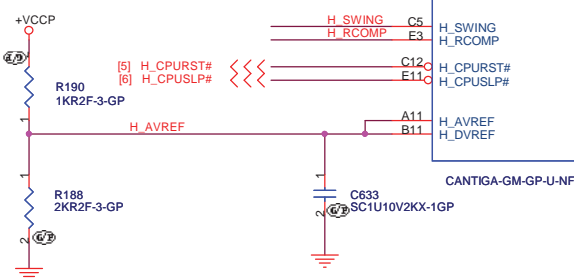
CPU (3 of 3)

Artemis

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Place them near to the chip (< 0.5")



VOX

Place the 49D9 Ohm resistor within 500 mils (1.27 mm) of the (G)MCH.

D

D

C

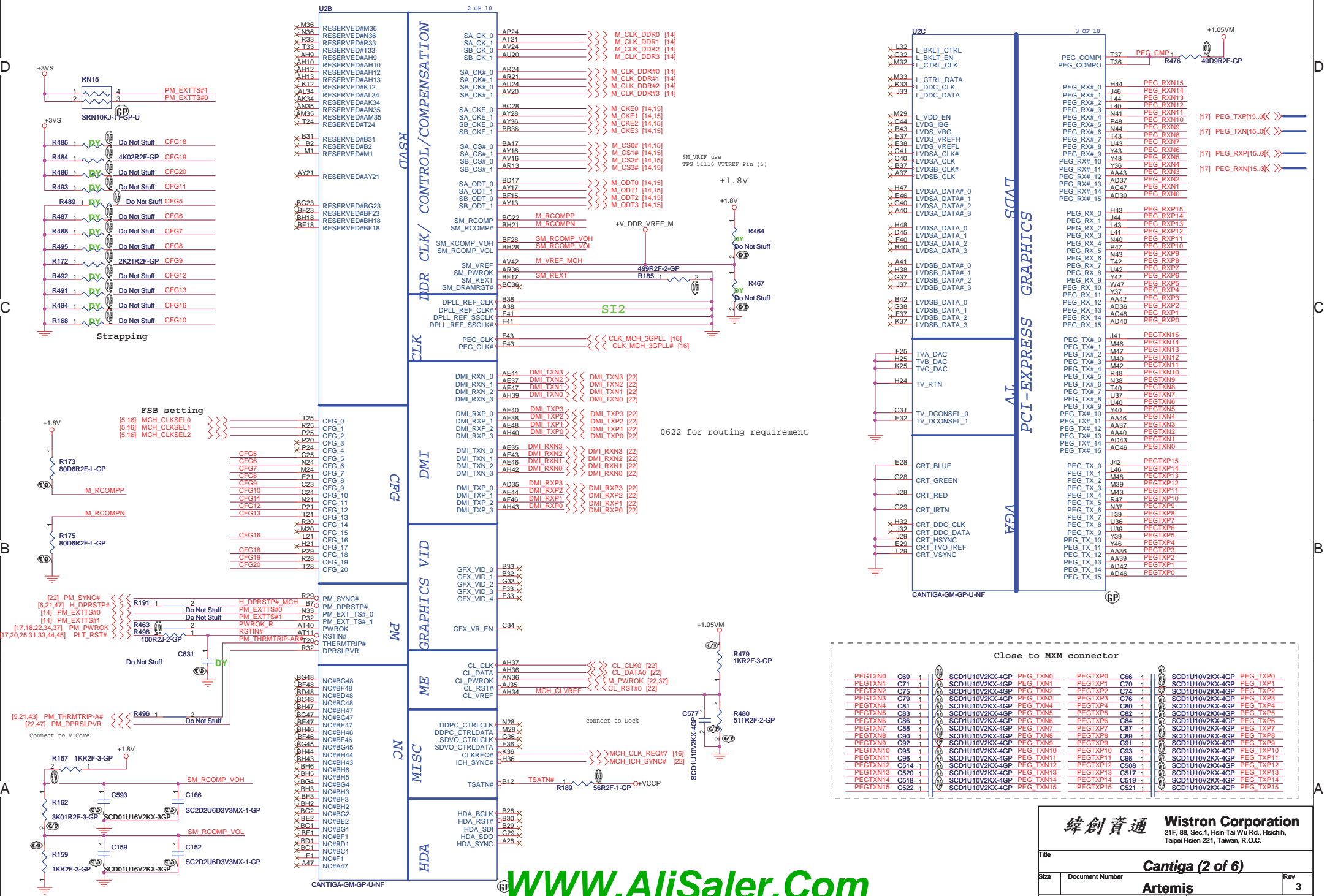
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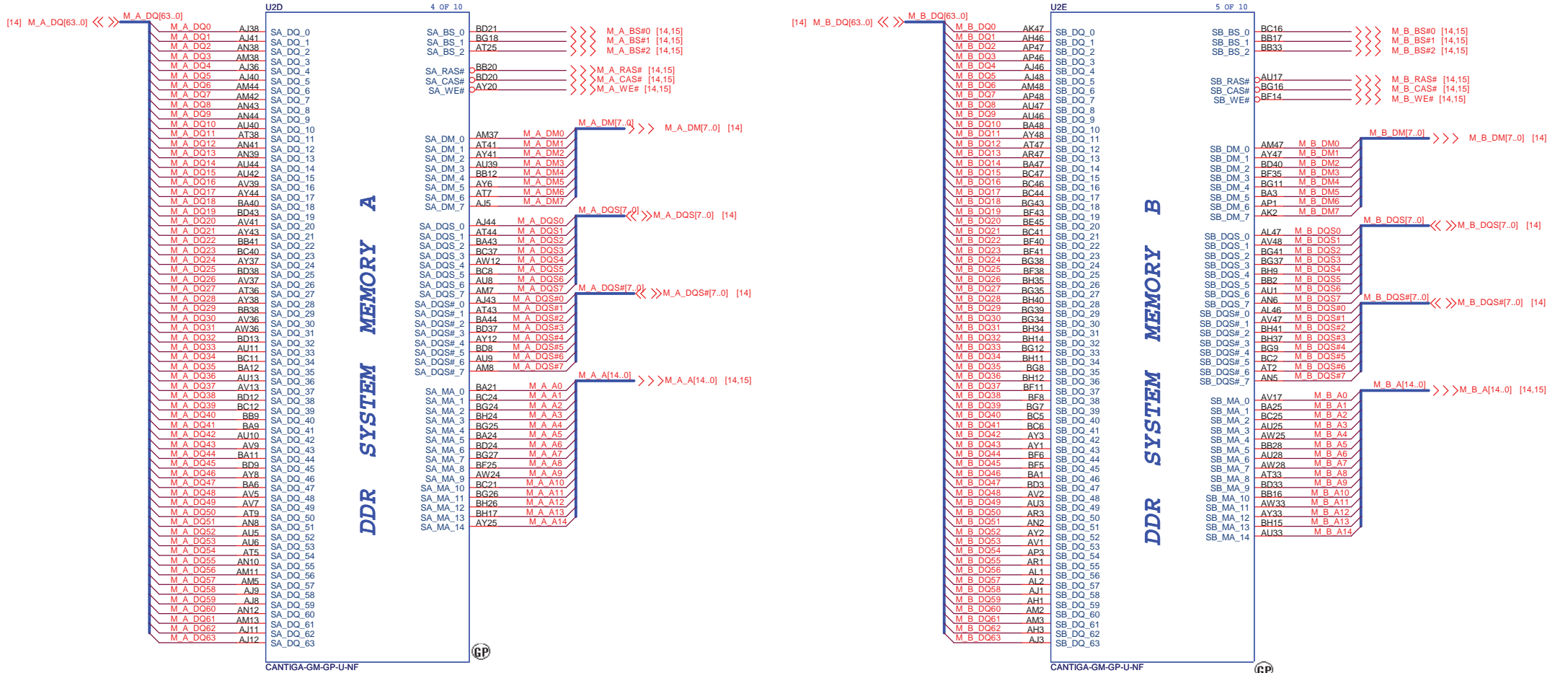
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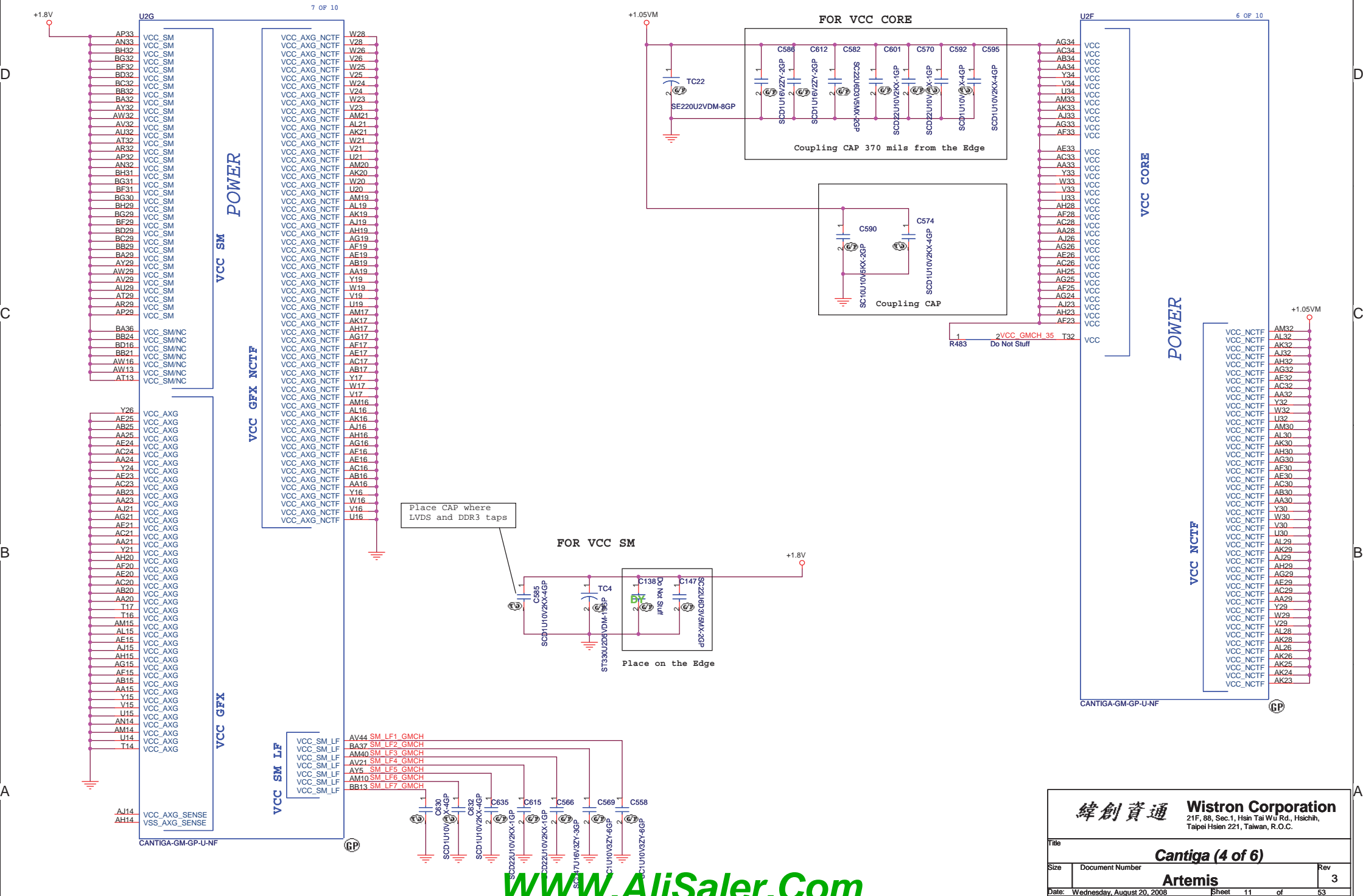
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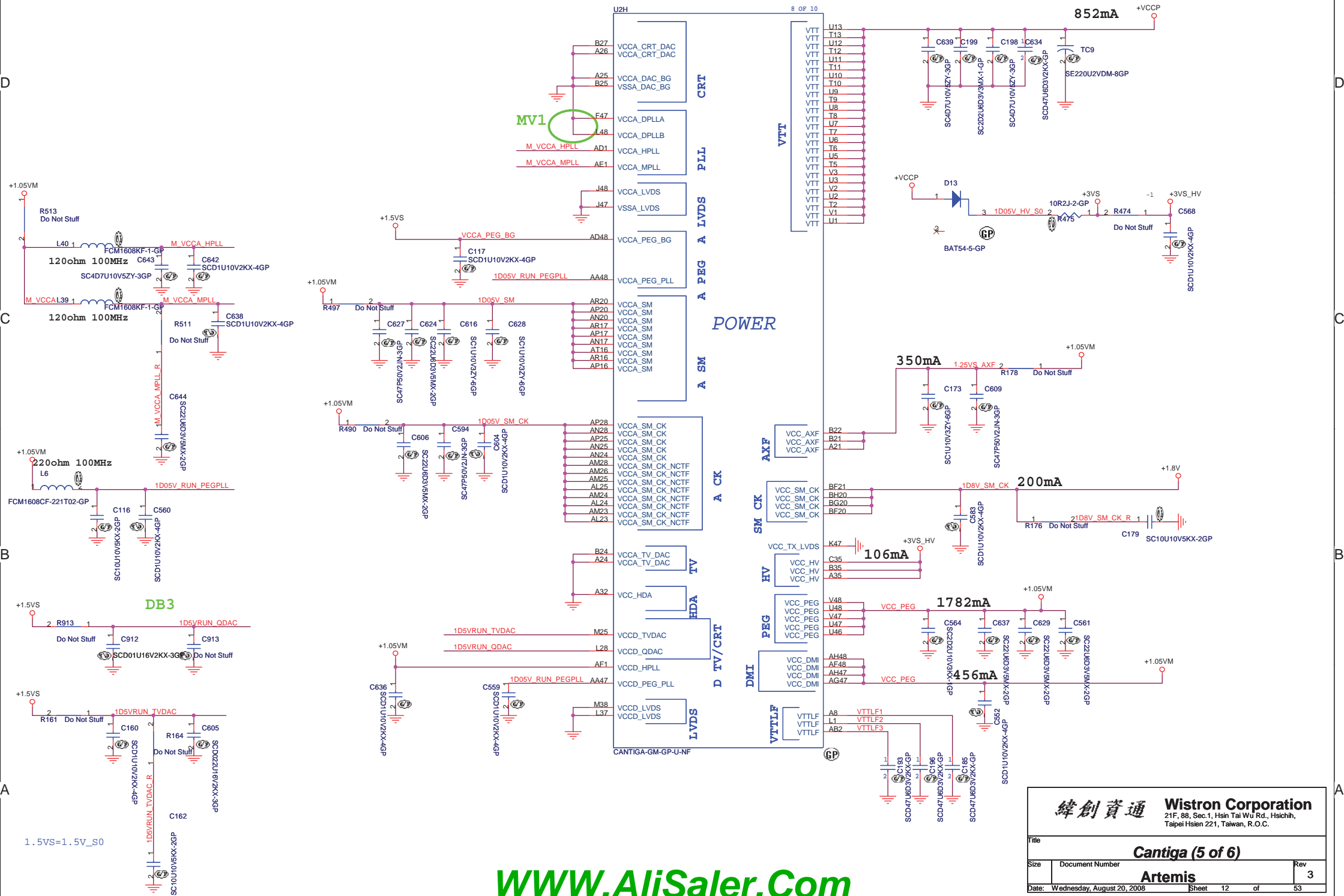
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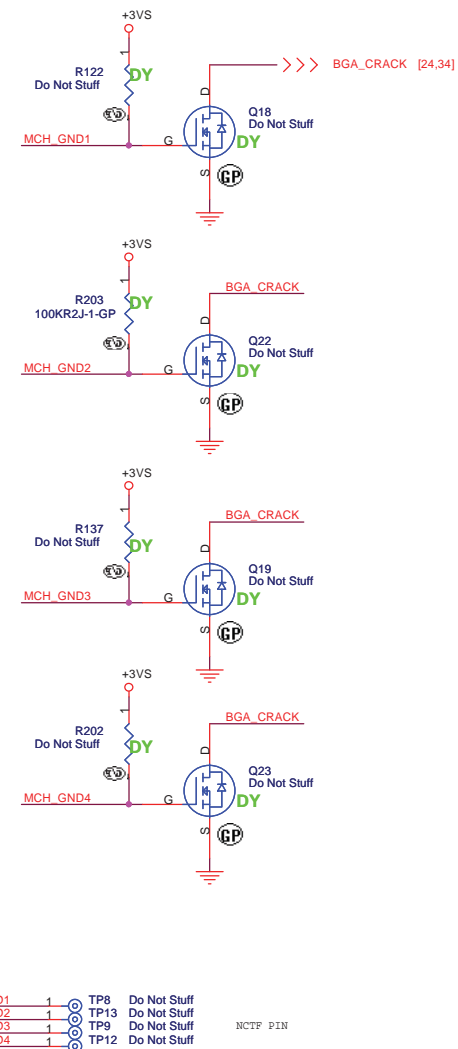
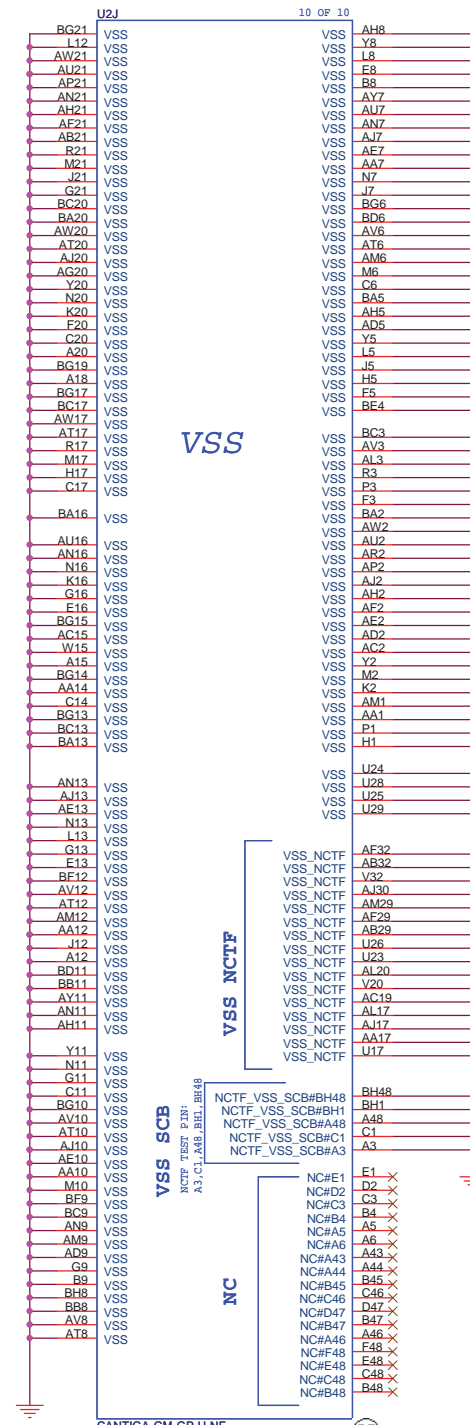
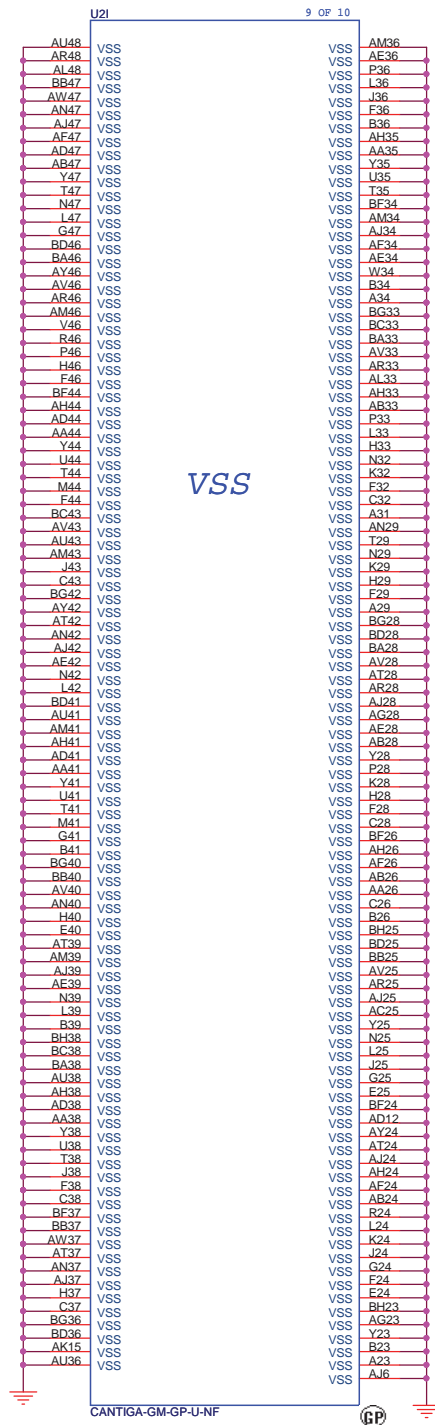
A











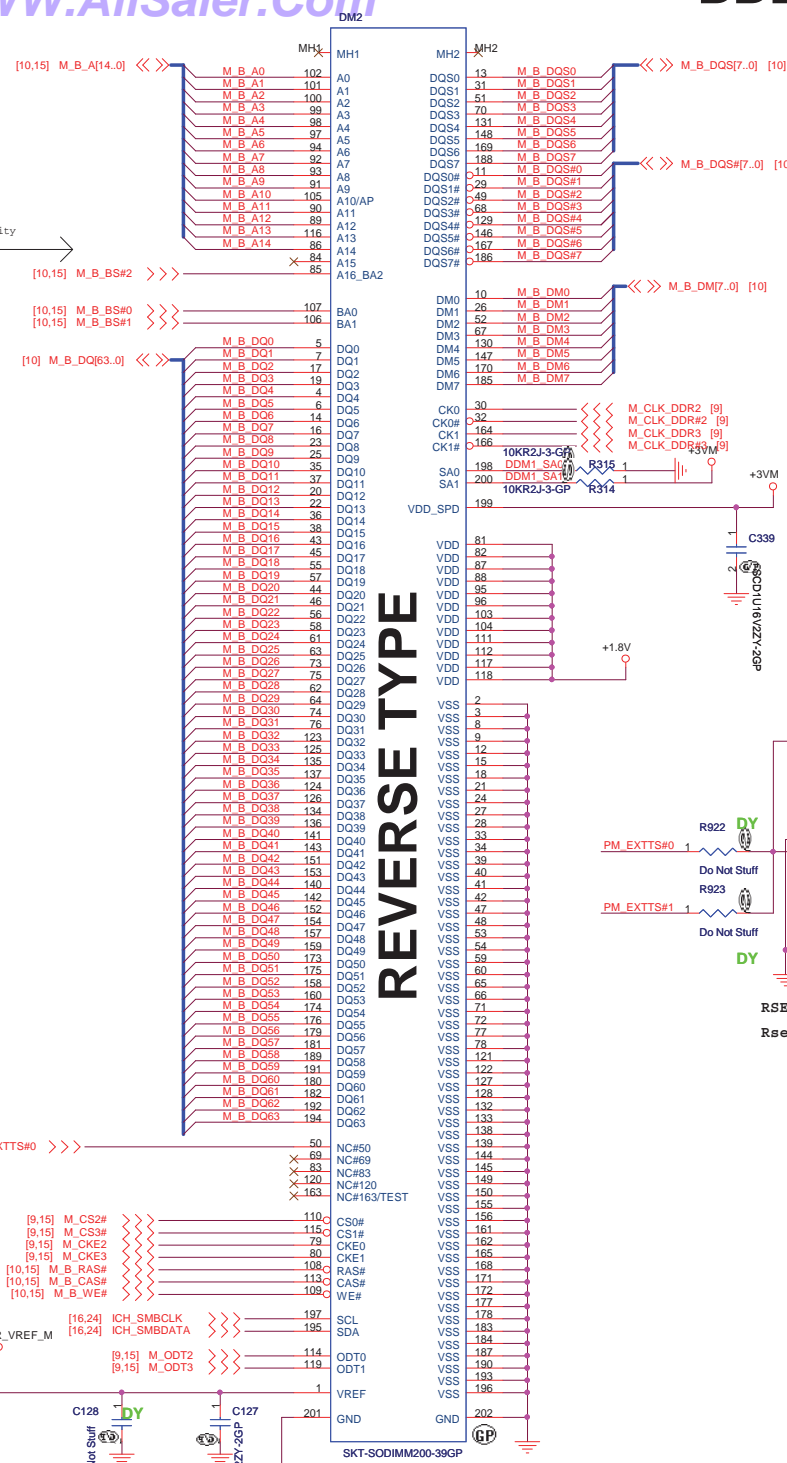
D

Follow Intel circuitry

C

B

A



Place near DM2

Follow Intel circuitry

0722

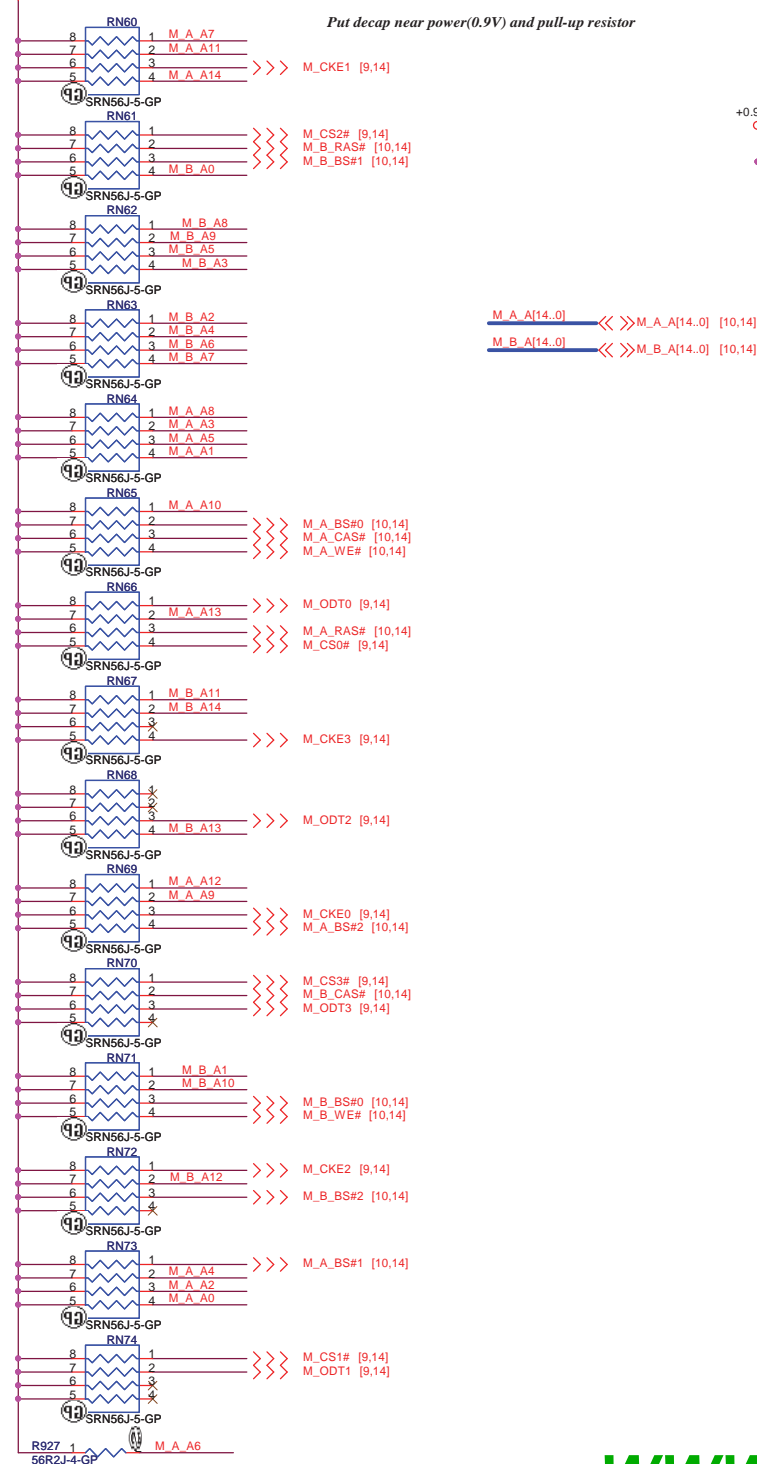
$$RSET = 0.0012T2 - 0.9308T + 96.147$$

$$Rset = 30 \text{ Ohm Temp} = 80 \text{ degree C}$$

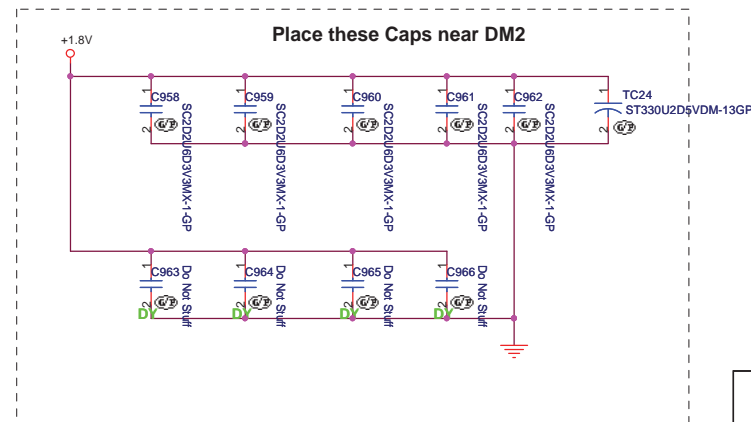
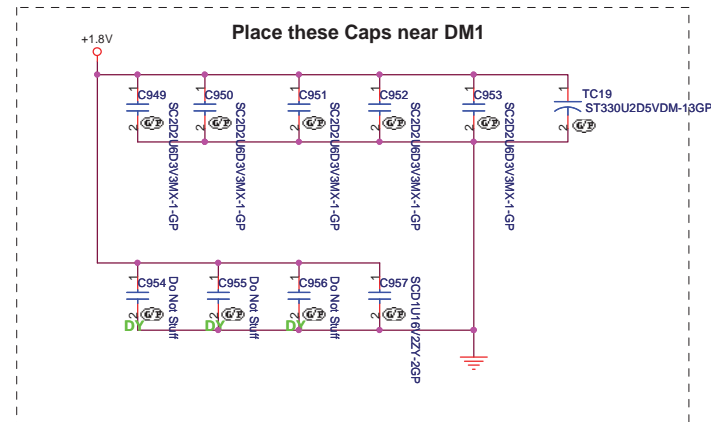
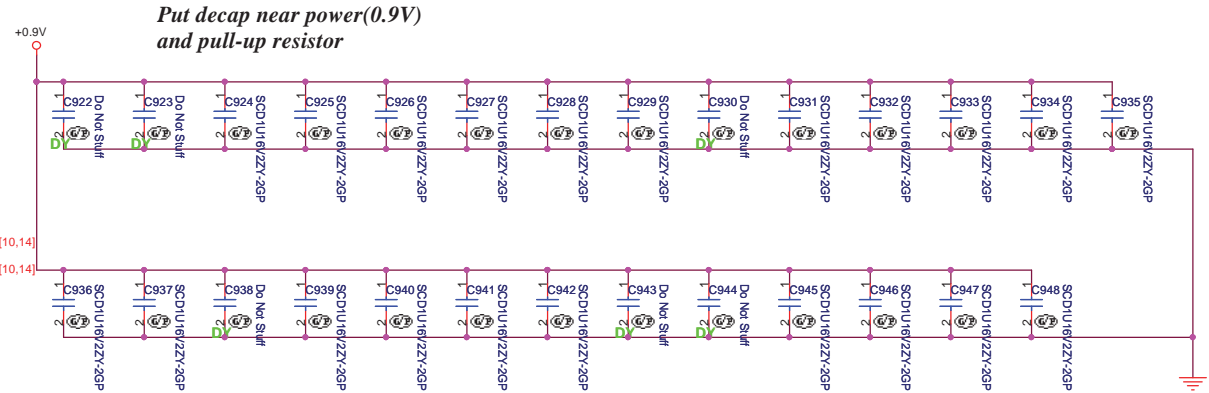


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DDR2 Socket			
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Decoupling Capacitor

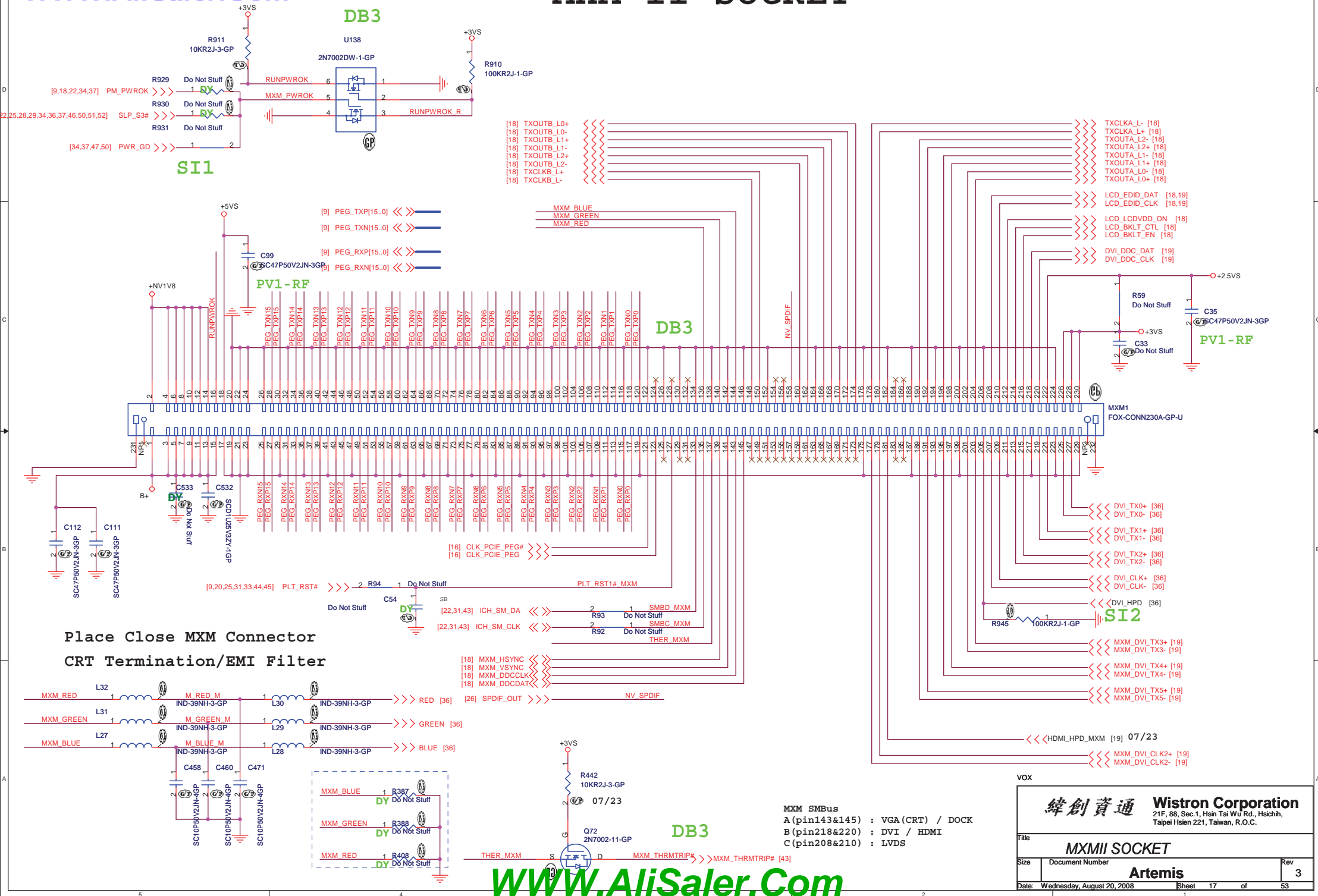


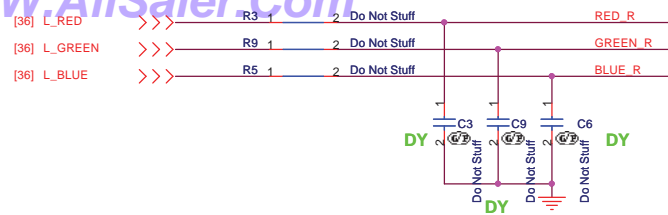
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Title			DDR2 Termination Resistor
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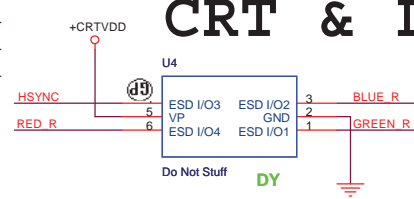


MXM II SOCKET

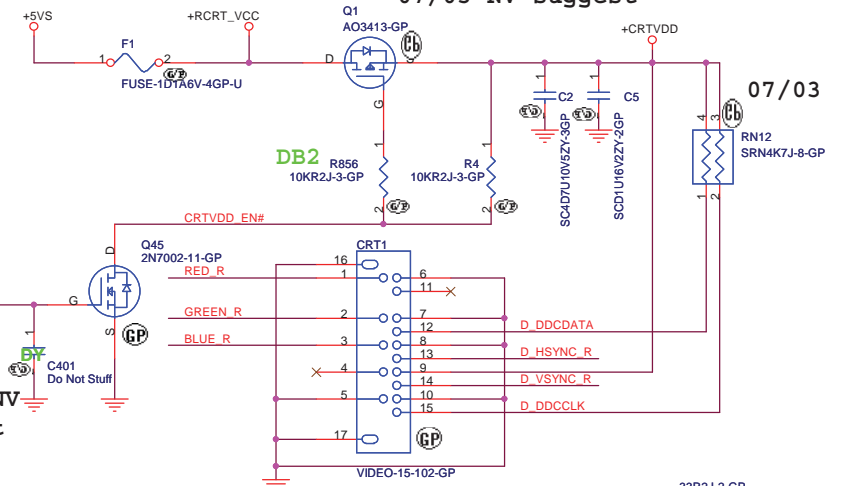




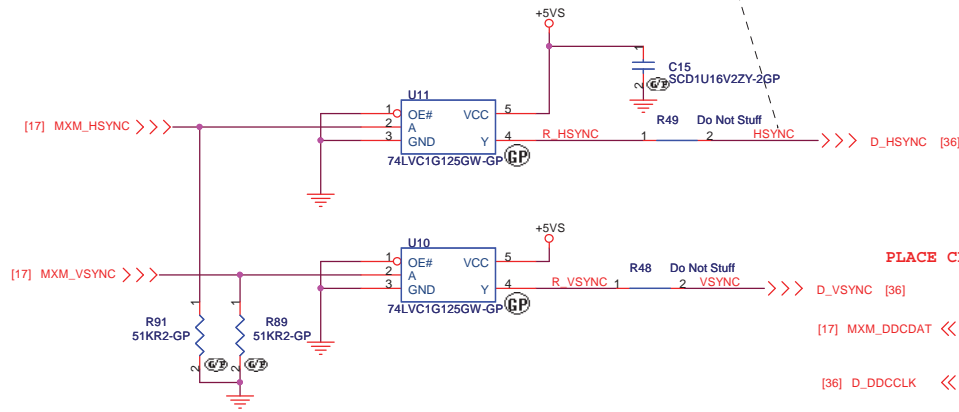
CRT & LCD



07/03 NV suggest



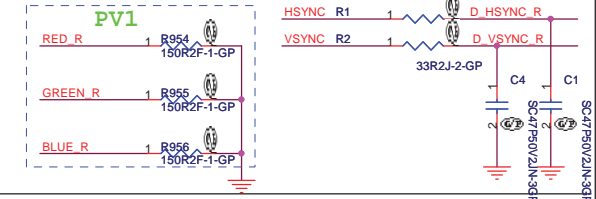
Layout Note : HSYNC & VSYNC SHOULD BE ROUTED TO DOCK CRT CONN. , THEN TO SYSTEM CRT CONN.



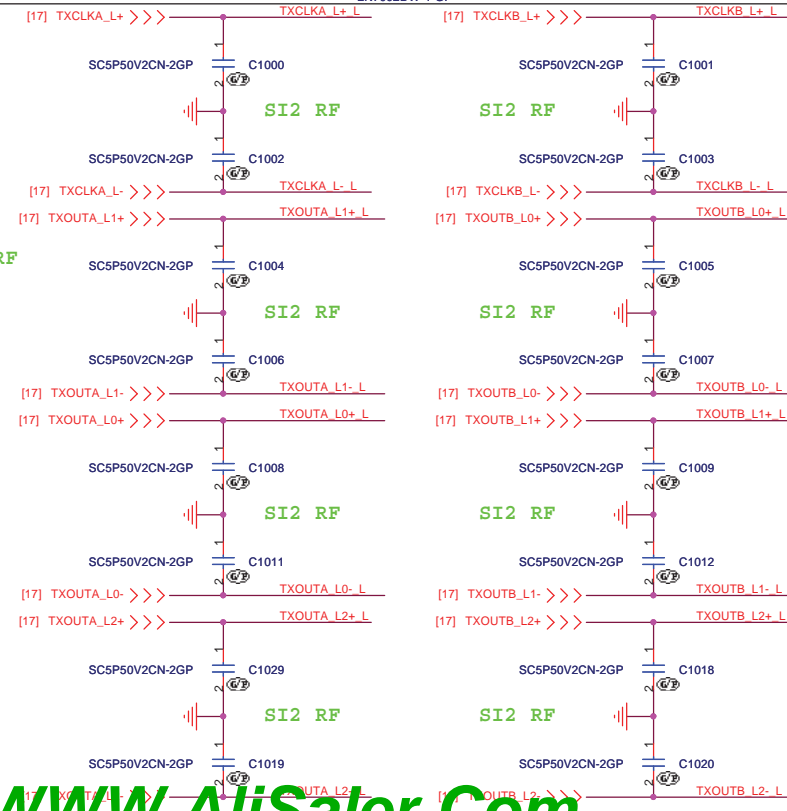
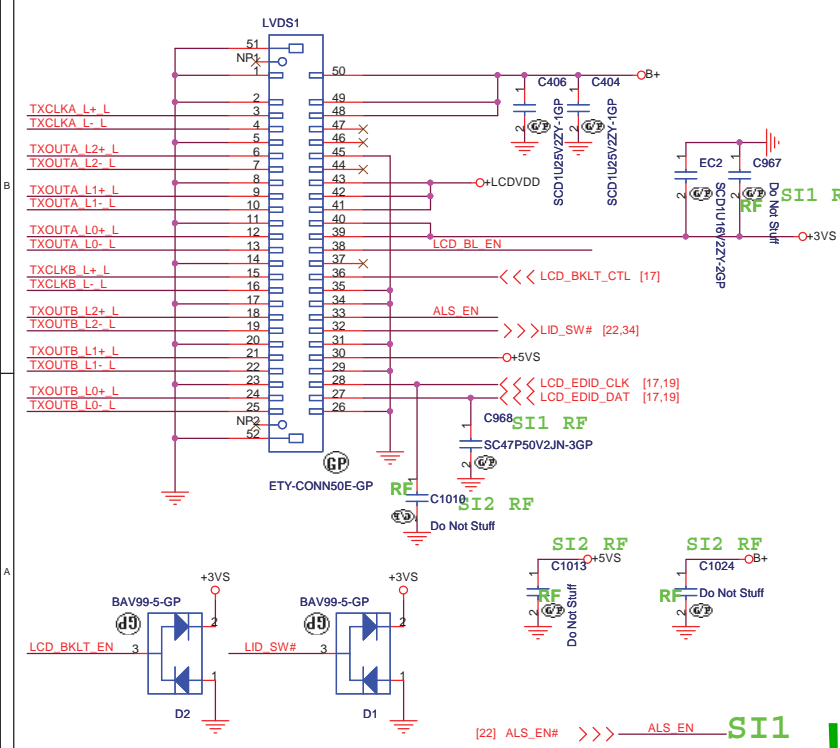
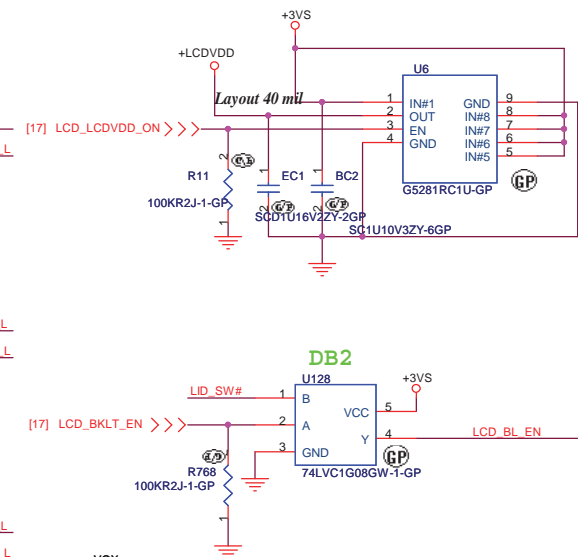
07/28

07/03 N
suggest

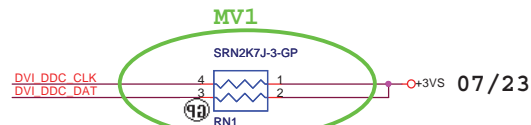
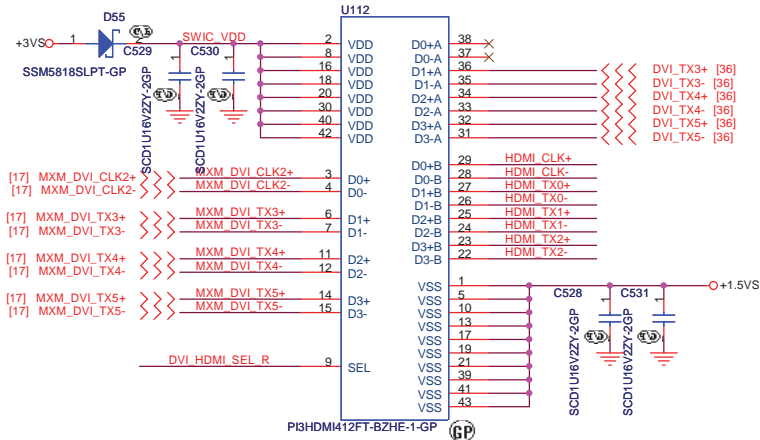
07/03



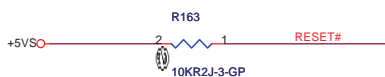
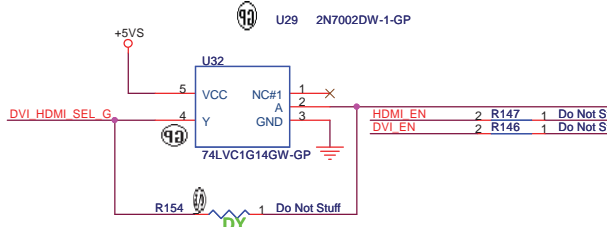
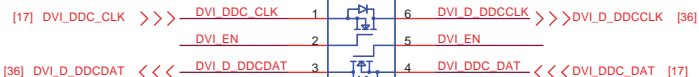
LCD POWER CIRCUIT



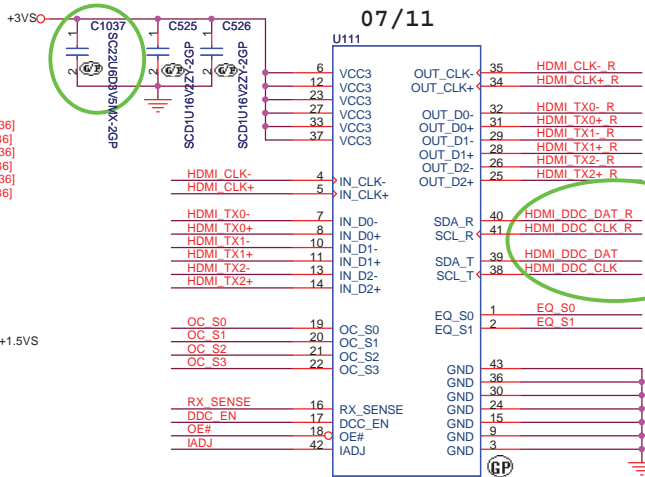
07/23



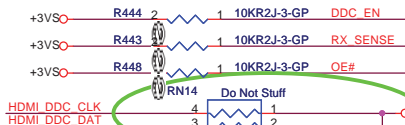
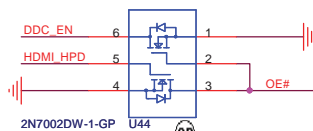
SI1



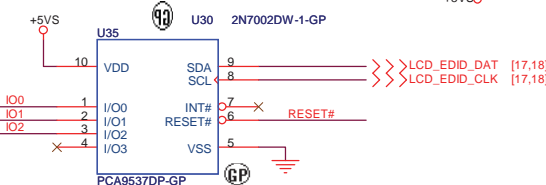
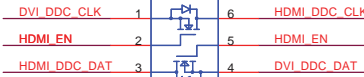
MV2



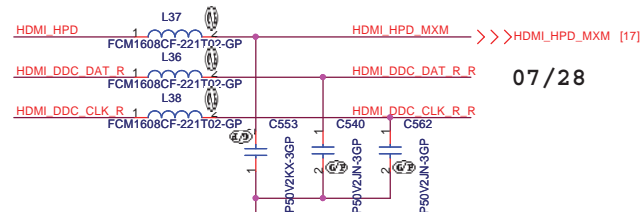
DB3



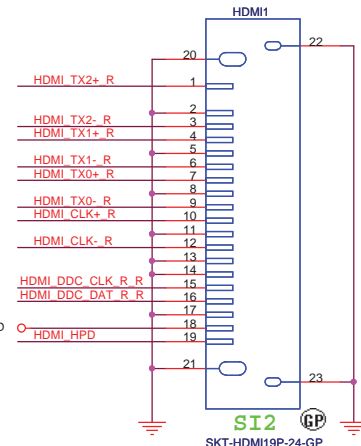
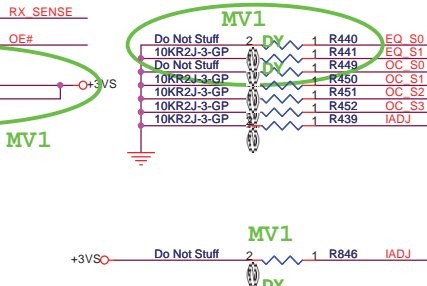
SI1



07/04 NV request



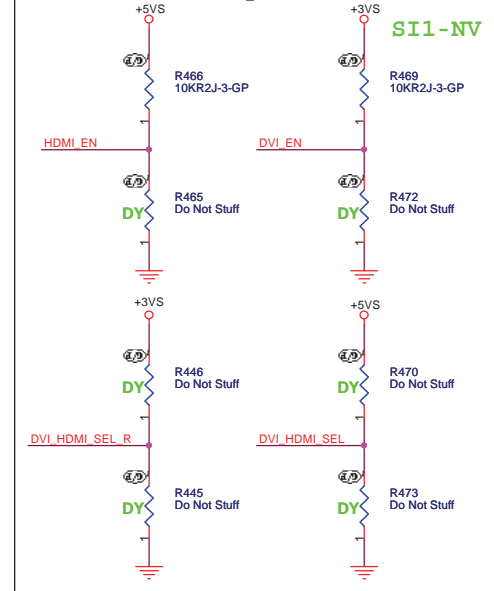
07/28



07/03 NV request



Function option, reserve

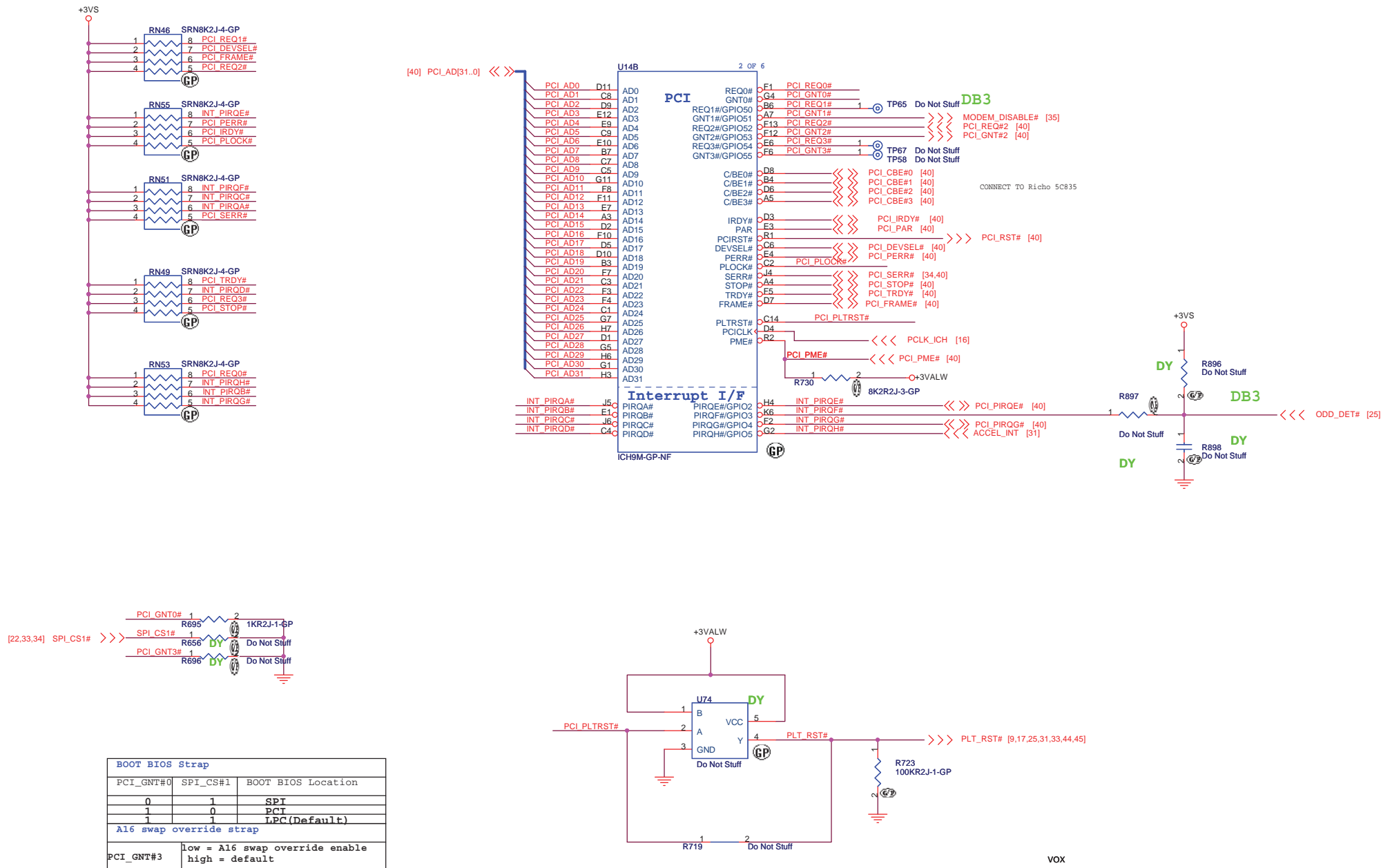


VOX

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ICH9-M (1 of 5) PCI

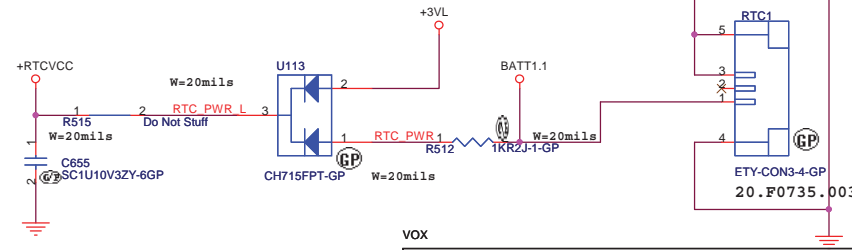
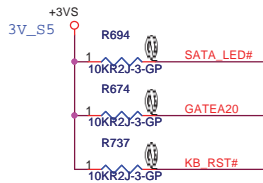
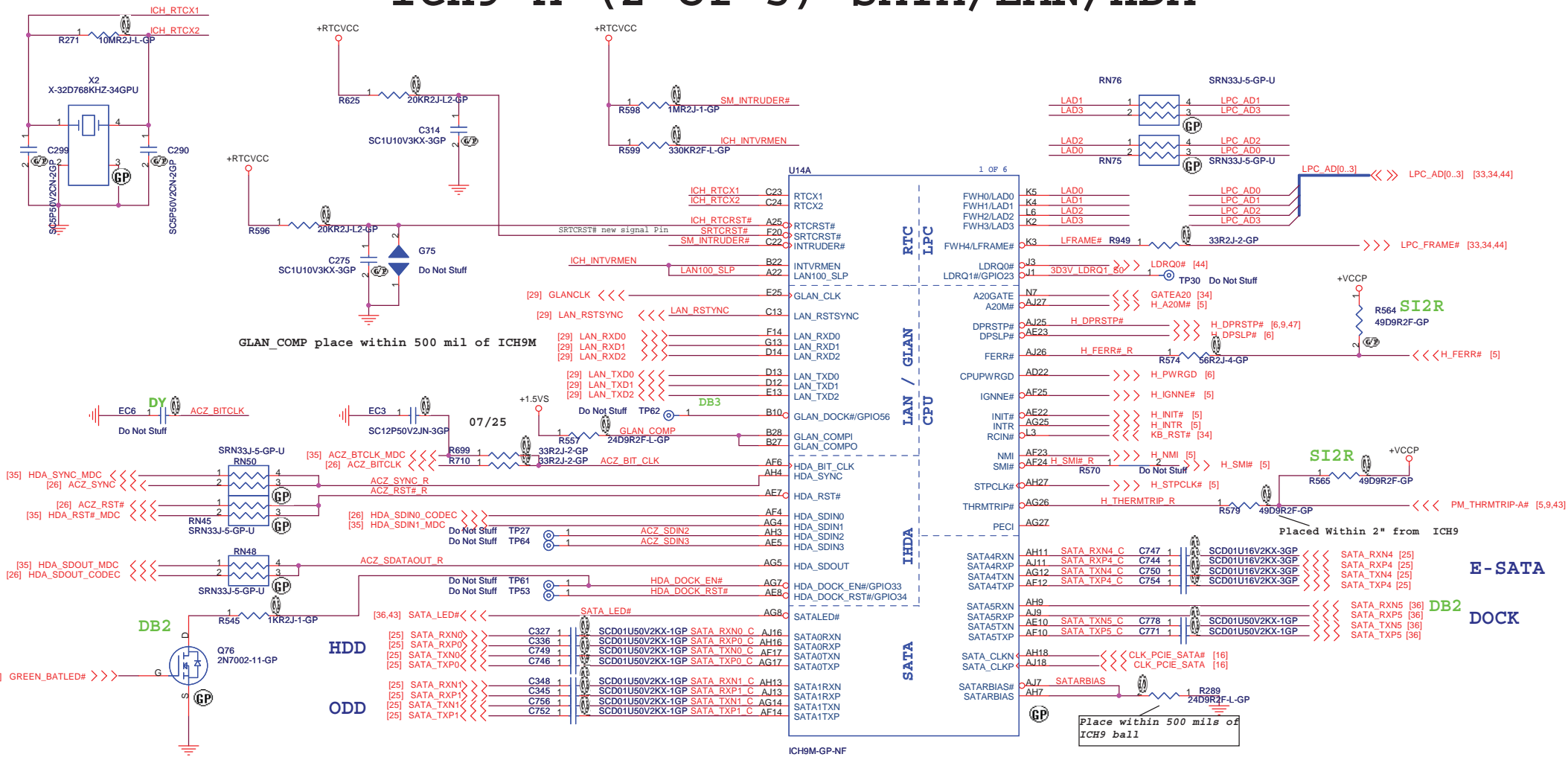


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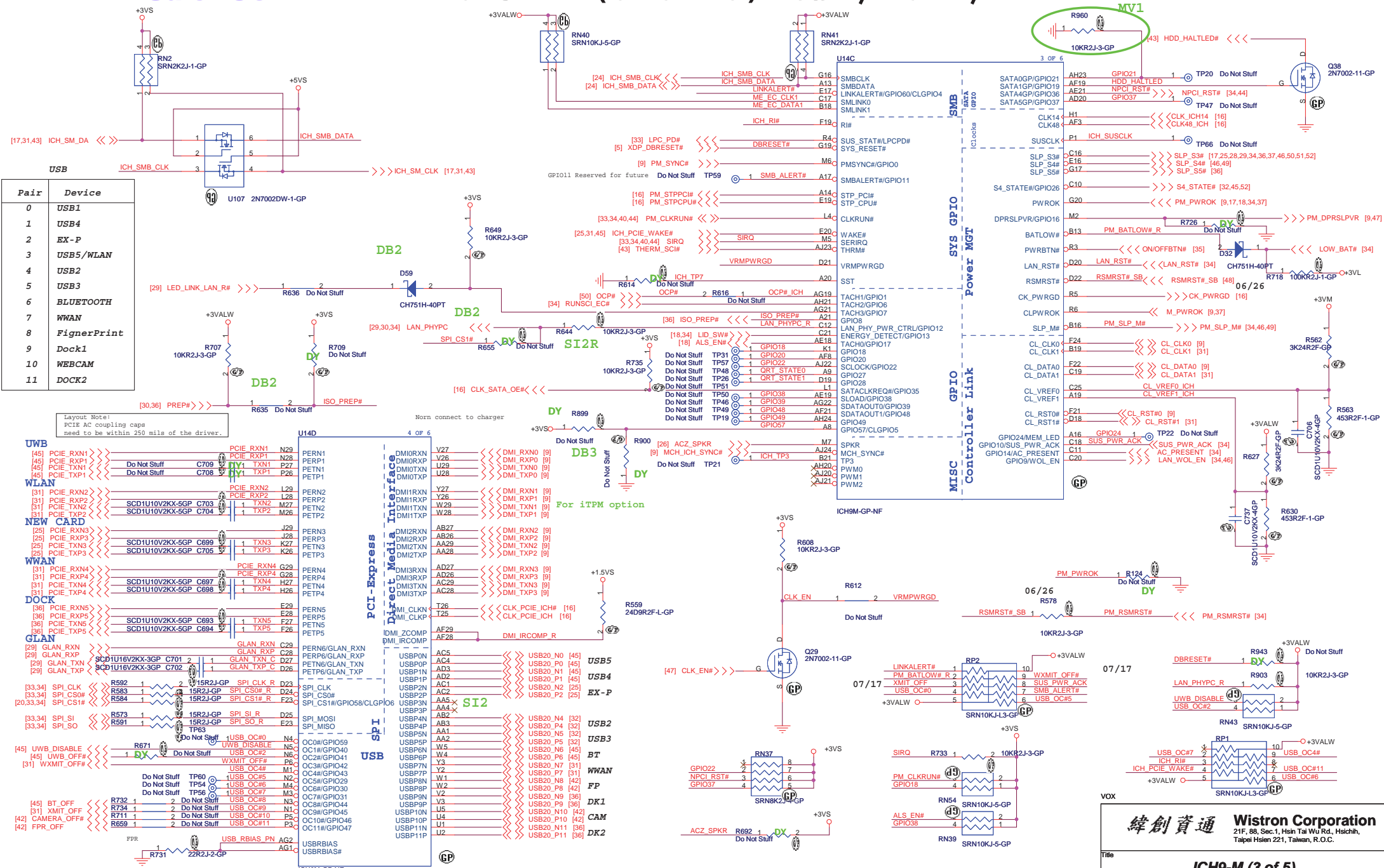
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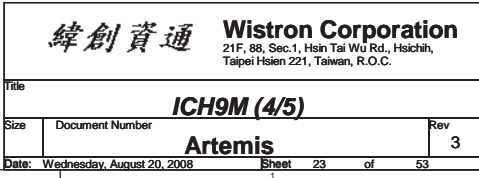
ICH9-M (2 of 5) SATA/LAN/HDA



integrated VccSns1_05,VccSns1_5,VccCL1_5	
INTVRMEN	High=Enable Low=Disable
integrated VccLan1_05VccCL1_05	
LAN100_SLP	High=Enable Low=Disable

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Title ICH9-M (2 of 5)			
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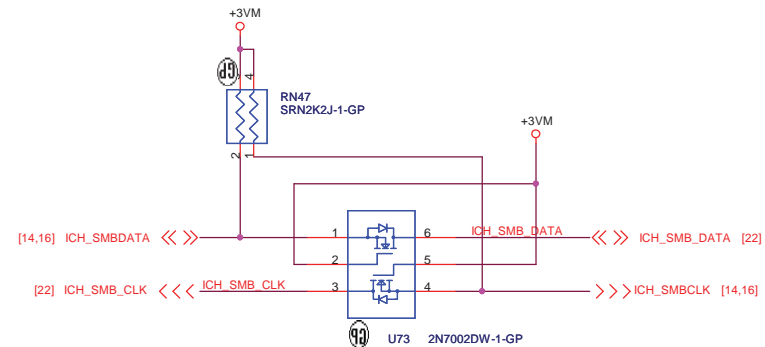
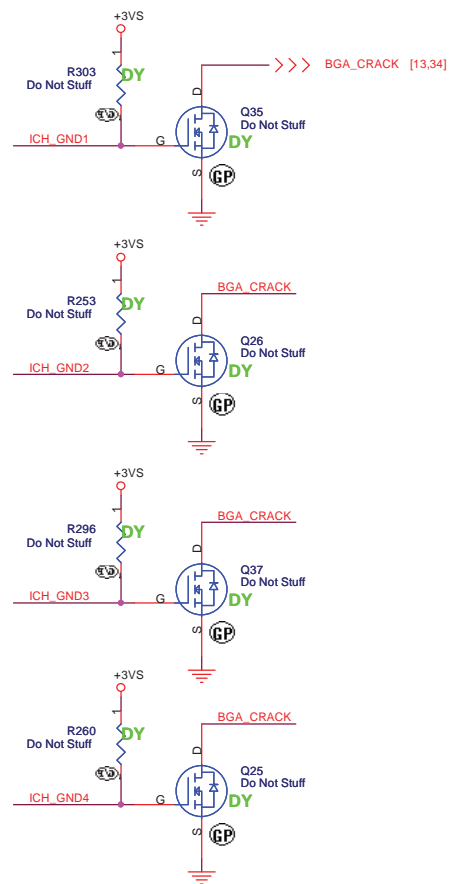


AA26	VSS	H5	VSS
AA27	VSS	J23	VSS
AA3	VSS	J26	VSS
AA6	VSS	J27	VSS
AB1	VSS	AC22	VSS
AA23	VSS	K28	VSS
AB28	VSS	K29	VSS
AB29	VSS	L13	VSS
AB4	VSS	L15	VSS
AB5	VSS	L2	VSS
AC17	VSS	L26	VSS
AC26	VSS	L27	VSS
AC27	VSS	L5	VSS
AC3	VSS	L7	VSS
AD1	VSS	M12	VSS
AD10	VSS	M13	VSS
AD12	VSS	M14	VSS
AD13	VSS	M15	VSS
AD14	VSS	M16	VSS
AD17	VSS	M17	VSS
AD18	VSS	M23	VSS
AD21	VSS	M28	VSS
AD28	VSS	M29	VSS
AD29	VSS	N11	VSS
AD4	VSS	N12	VSS
AD5	VSS	N13	VSS
AD6	VSS	N14	VSS
AD7	VSS	N15	VSS
AD9	VSS	N16	VSS
AE12	VSS	N17	VSS
AE13	VSS	N18	VSS
AE14	VSS	N26	VSS
AE16	VSS	N27	VSS
AE17	VSS	P12	VSS
AE2	VSS	P13	VSS
AE20	VSS	P14	VSS
AE24	VSS	P15	VSS
AE3	VSS	P16	VSS
AE4	VSS	P17	VSS
AE6	VSS	P2	VSS
AE9	VSS	P23	VSS
AF13	VSS	P28	VSS
AF16	VSS	P29	VSS
AF18	VSS	P4	VSS
AF22	VSS	P7	VSS
AH26	VSS	R12	VSS
AF26	VSS	R13	VSS
AF27	VSS	R14	VSS
AF5	VSS	R15	VSS
AE7	VSS	R16	VSS
AE9	VSS	R17	VSS
AG13	VSS	R18	VSS
AG16	VSS	R28	VSS
AG18	VSS	T12	VSS
AG20	VSS	T13	VSS
AG23	VSS	T14	VSS
AG3	VSS	T15	VSS
AG6	VSS	T16	VSS
AG9	VSS	T17	VSS
AH12	VSS	T23	VSS
AH14	VSS	B26	VSS
AH17	VSS	U12	VSS
AH19	VSS	U13	VSS
AH2	VSS	U14	VSS
AH22	VSS	U15	VSS
AH25	VSS	U16	VSS
AH28	VSS	U17	VSS
AH5	VSS	AD23	VSS
AH8	VSS	U26	VSS
AJ12	VSS	U27	VSS
AJ14	VSS	U3	VSS
AJ17	VSS	V1	VSS
AJ8	VSS	V13	VSS
B11	VSS	V15	VSS
B14	VSS	V23	VSS
B17	VSS	V28	VSS
B2	VSS	V29	VSS
B20	VSS	V4	VSS
B23	VSS	V5	VSS
B5	VSS	W26	VSS
B8	VSS	W27	VSS
C26	VSS	W3	VSS
C27	VSS	Y1	VSS
E11	VSS	Y28	VSS
E14	VSS	Y29	VSS
E18	VSS	Y4	VSS
E2	VSS	Y5	VSS
E21	VSS	AG28	VSS
E24	VSS	AH6	VSS
E5	VSS	AE2	VSS
E8	VSS	B25	VSS
F16	VSS		
F18	VSS		
F21	VSS		
F24	VSS		
F28	VSS		
F29	VSS		
G12	VSS		
G14	VSS		
G18	VSS		
G21	VSS		
G24	VSS		
G26	VSS		
G27	VSS		
G8	VSS		
H2	VSS		
H23	VSS		
H28	VSS		
H29	VSS		

NCTF TEST PIN:
 A1, A2, B1, A28, A29, B9, B11, A21, A22, A23, A25, A26, A27

NCTF_VSS#A1 A1 TP29 Do Not Stuff
 NCTF_VSS#A2 A2
 NCTF_VSS#B1 B1
 NCTF_VSS#A29 A29 TP18 Do Not Stuff
 NCTF_VSS#A28 A28
 NCTF_VSS#B29 B29
 NCTF_VSS#A11 A11
 NCTF_VSS#A12 A12
 NCTF_VSS#AH1 AH1
 NCTF_VSS#AJ28 AJ28
 NCTF_VSS#AJ29 AJ29
 NCTF_VSS#AH29 AH29

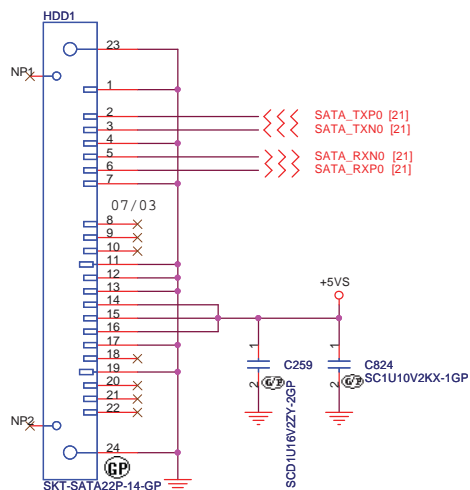
07/20 BGA CRACK CIRCUIT



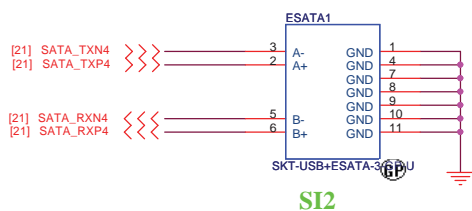
Q13 & Q14 connect SMLINK and SMBUS in S) for SMBus 2.0 compliance

SMBUS

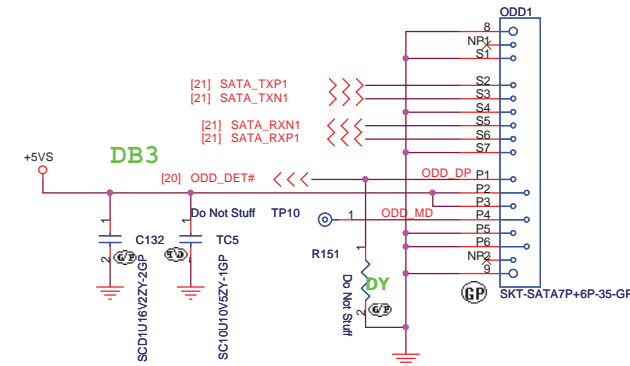
SATA HDD Connector



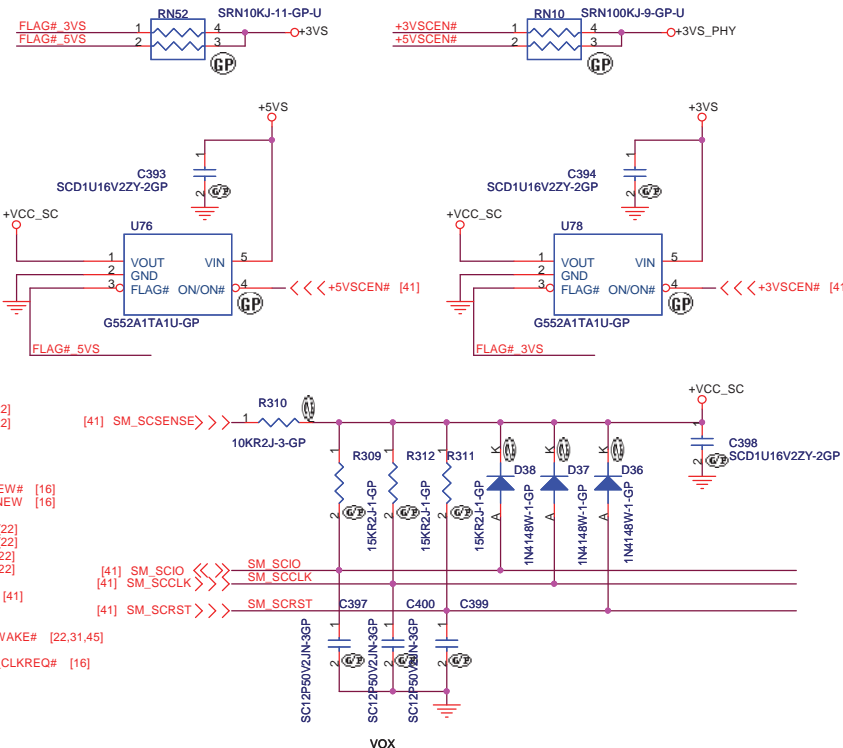
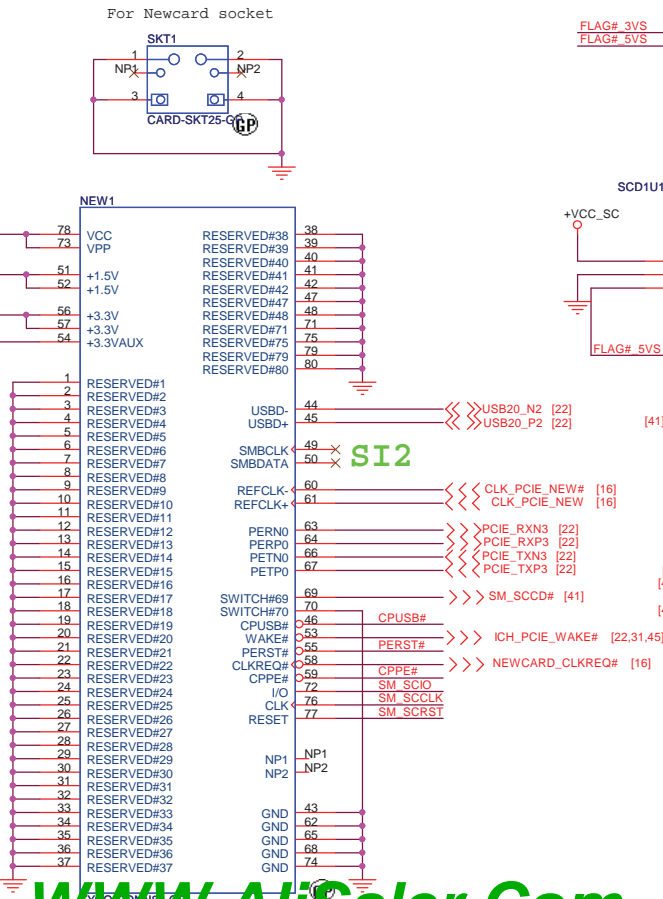
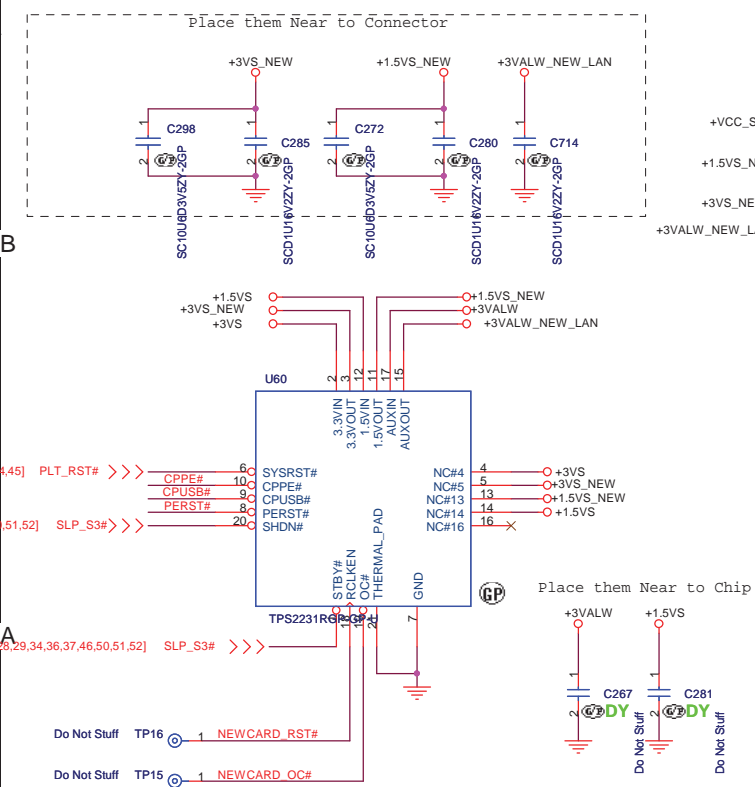
ESATA Connector

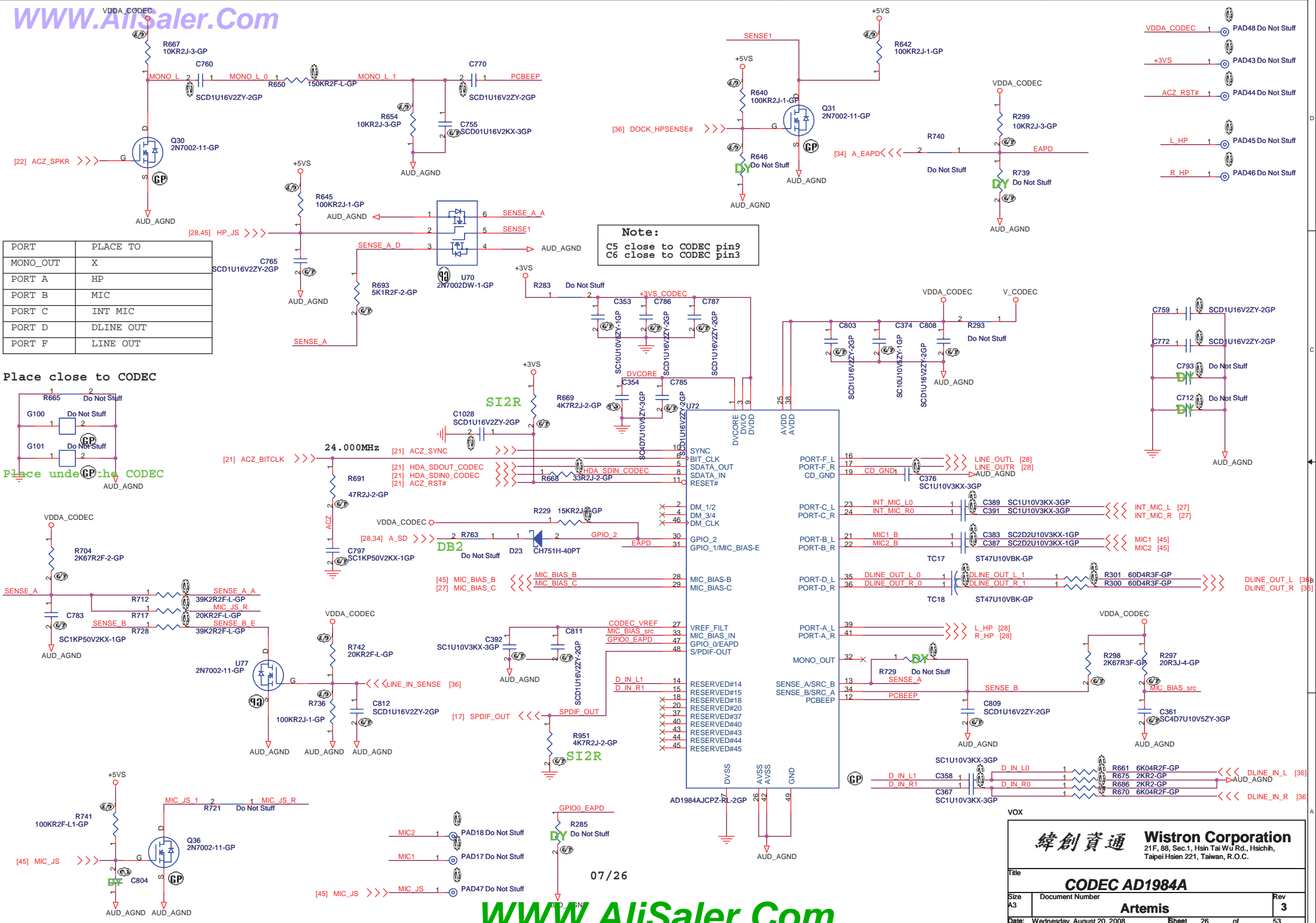


ODD Connector



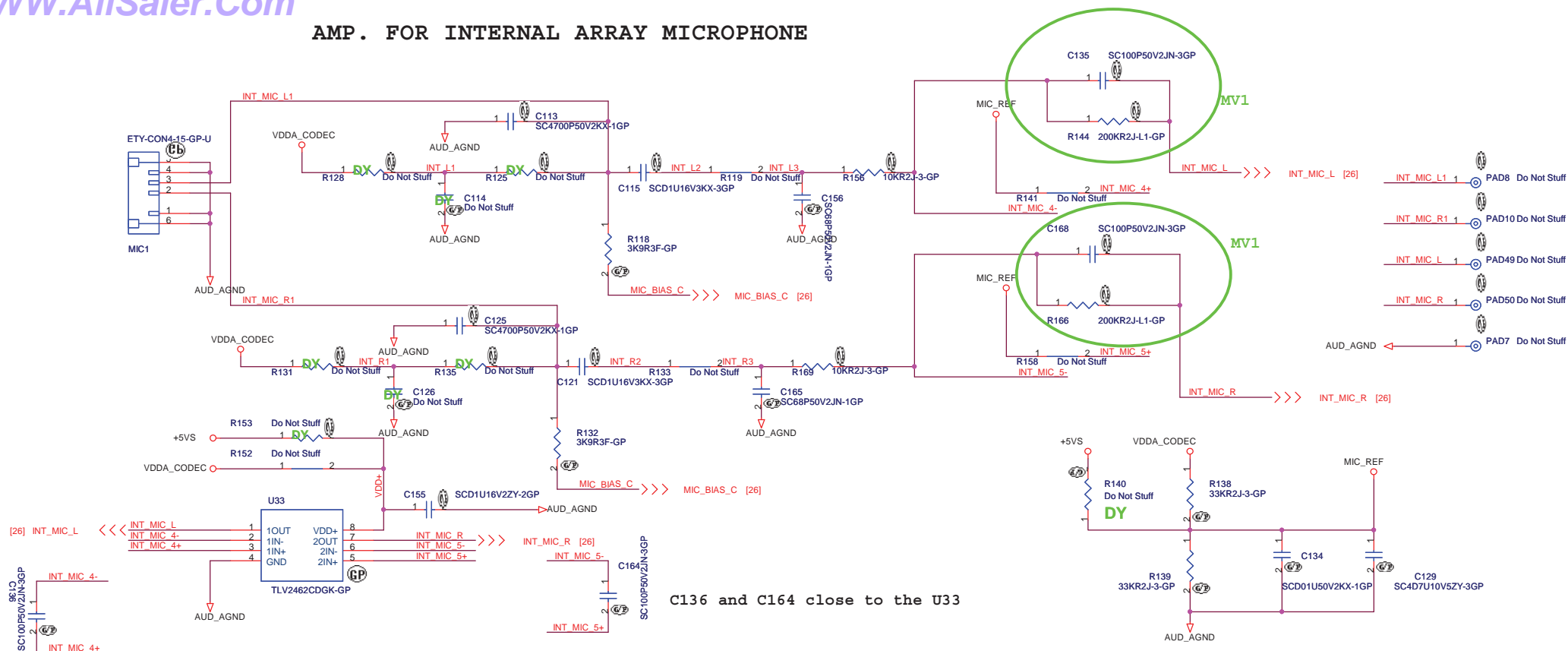
NEWCARD/SMART CARD Connector





緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
CODEC AD1984A		
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Date: Wednesday, August 20, 2008 Sheet 26 of 53		

AMP. FOR INTERNAL ARRAY MICROPHONE



C136 and C164 close to the U33

VOX

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

Title

INT MIC

Size

Document Number	
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Artemis

Rev

Date: Wednesday, August 20, 2008

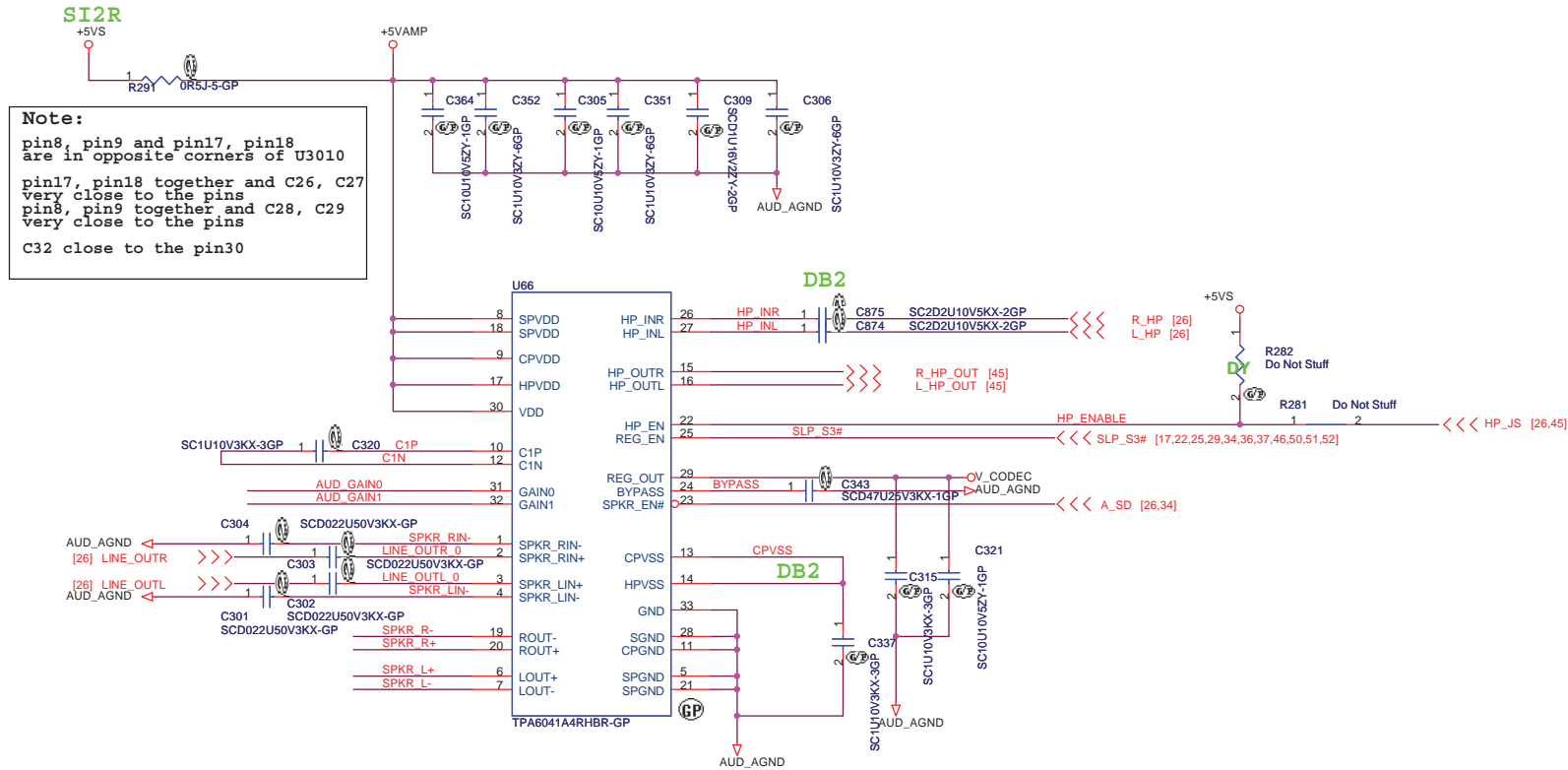
Sheet

27

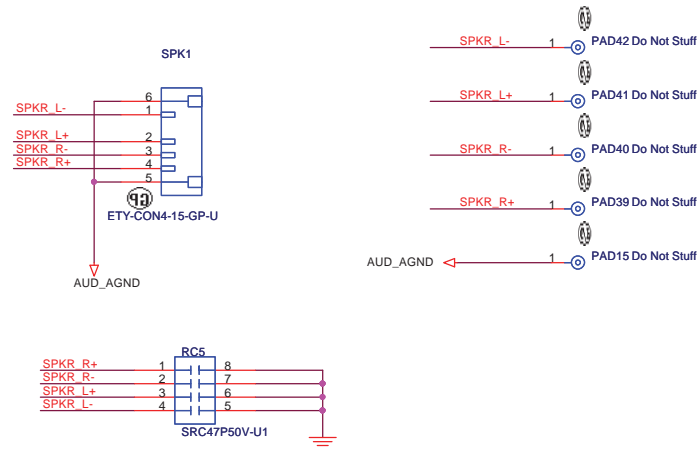
of

5

[illegible]

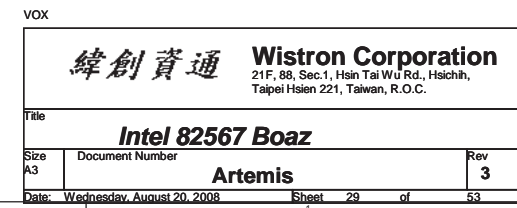


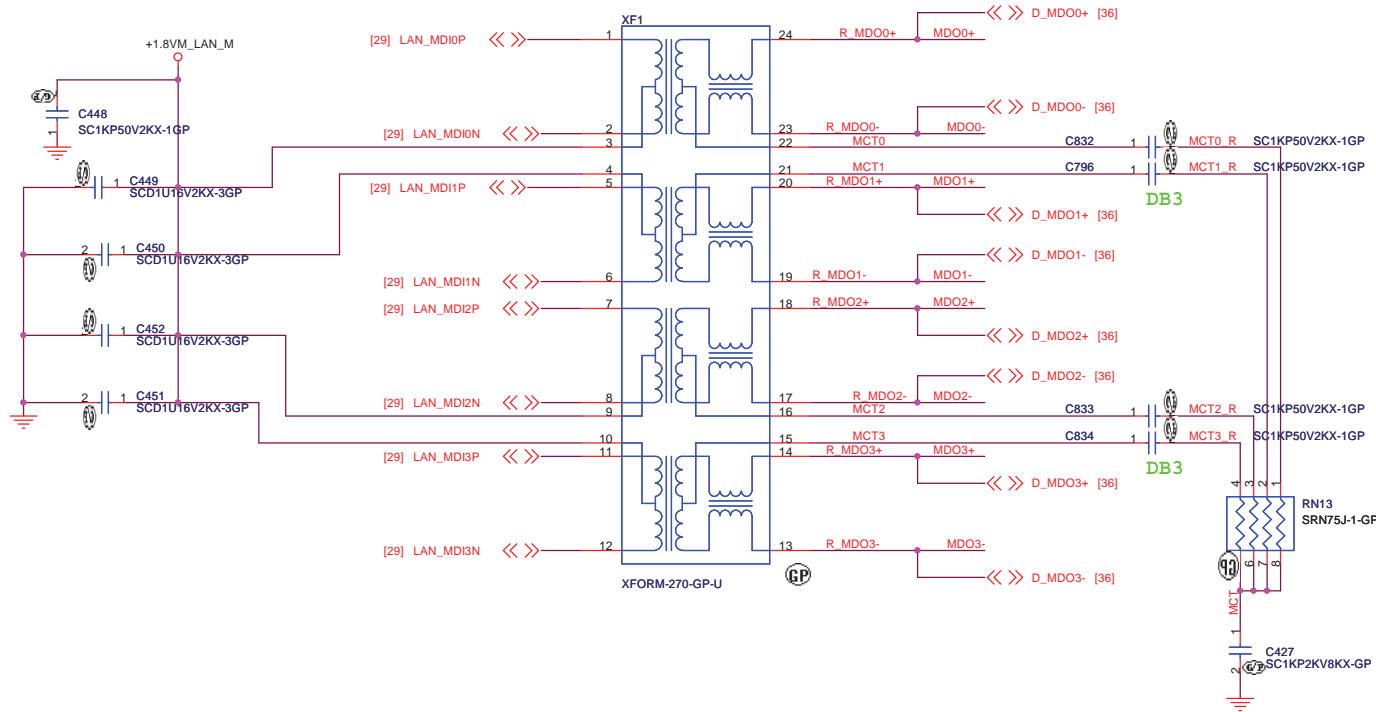
Speaker CONN.



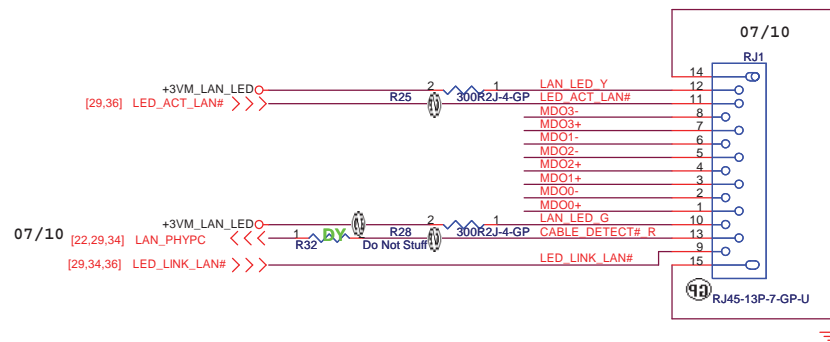
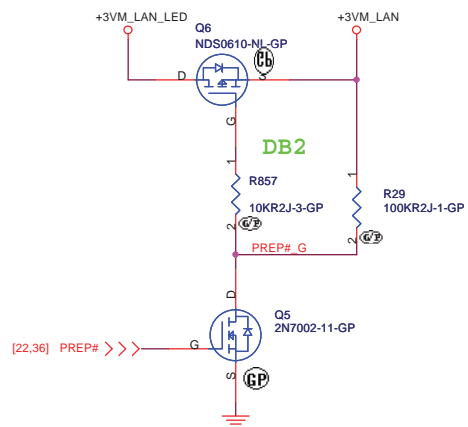
VOX

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Title			
AMP & SPEAKER			
Size A3	Document Number	Artemis	Rev 3
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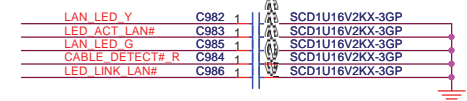




LAN ENERGY DET



SI2-EMI

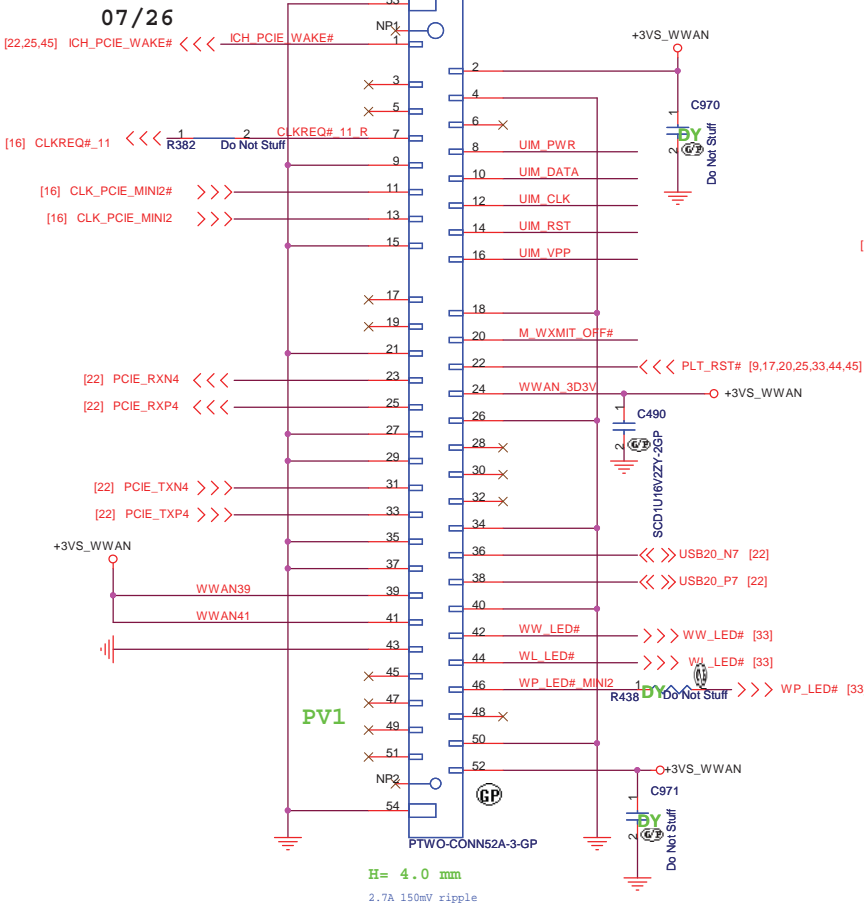


VOX

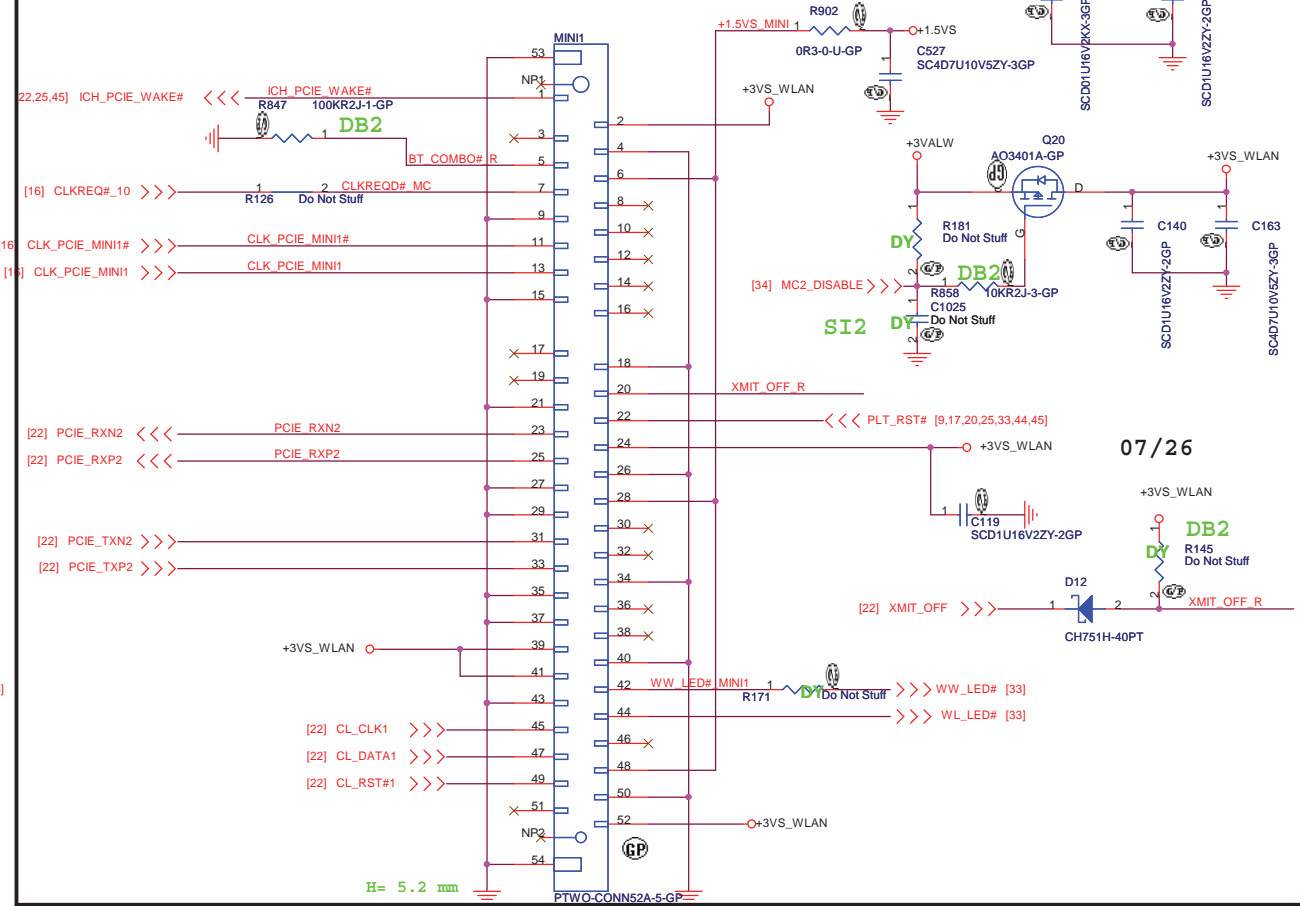
緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title			Magnetic & RJ45		
Size	Document Number	Artemis			Rev
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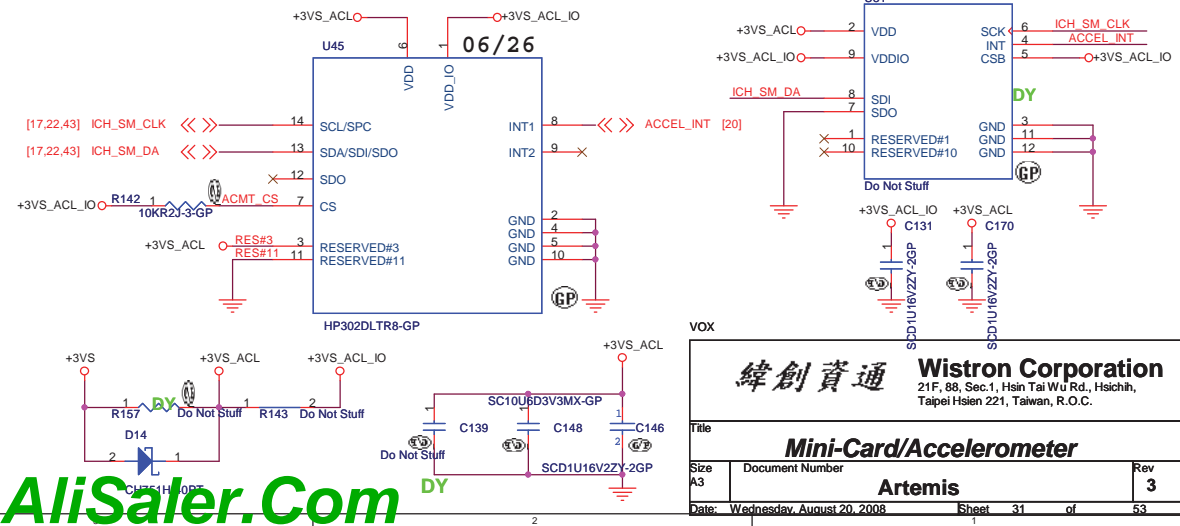
Mini-Card--WWAN BOTTOM



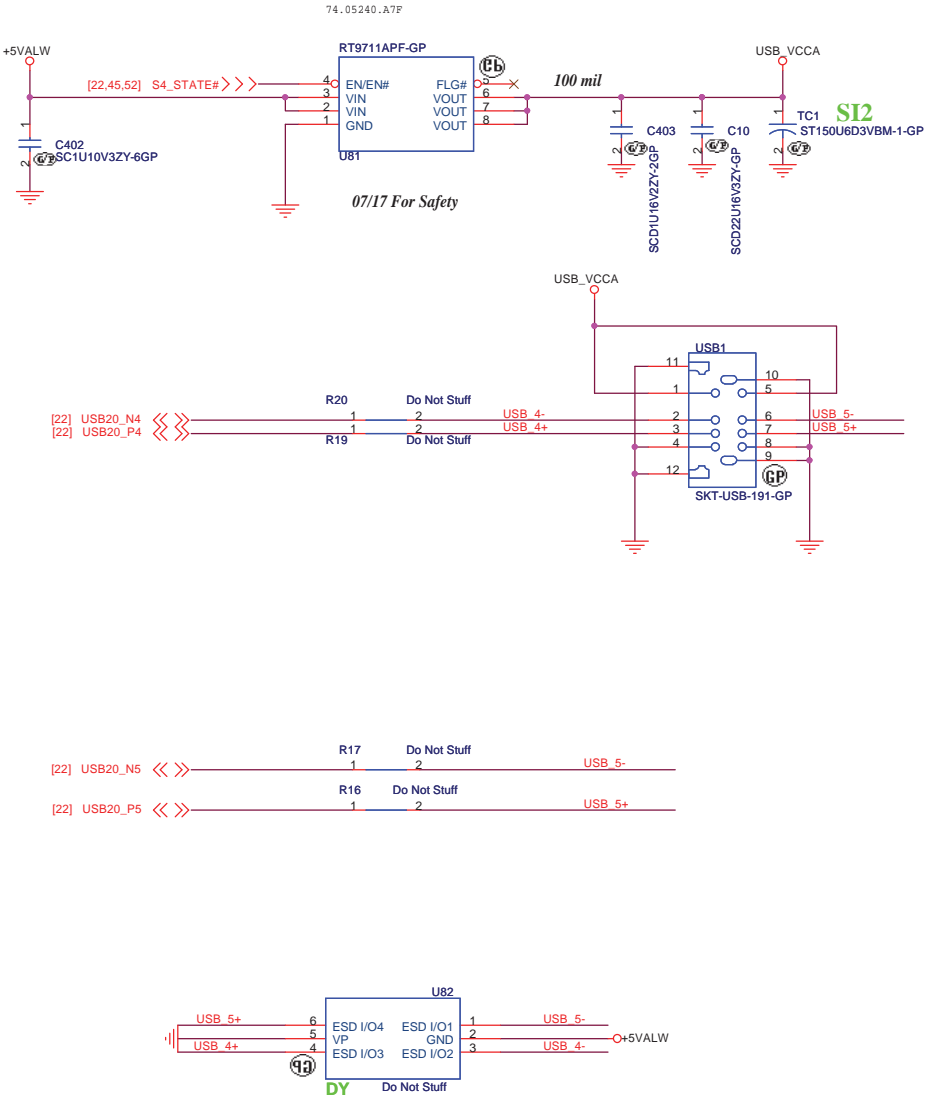
Mini-Card--WLAN TOP



ACCELEROMETER Must be placed in the center of the system

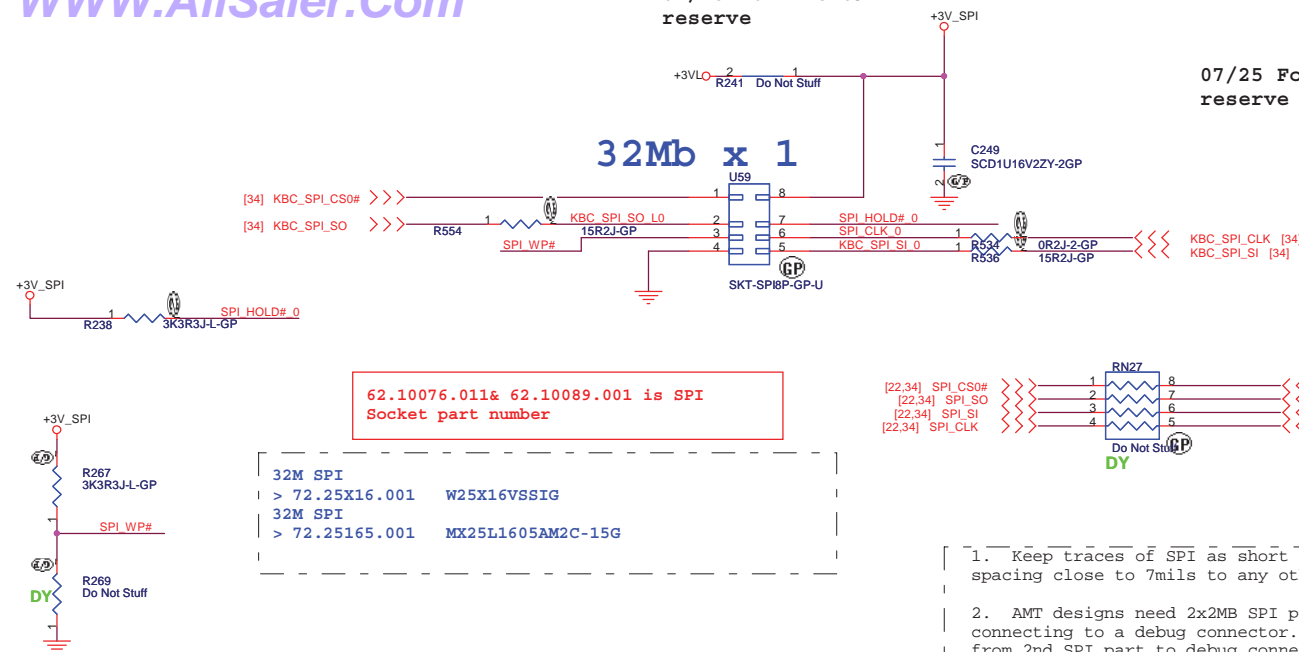


74.02231.073-->74.00577.A73,74.05538.073
2462--G1224

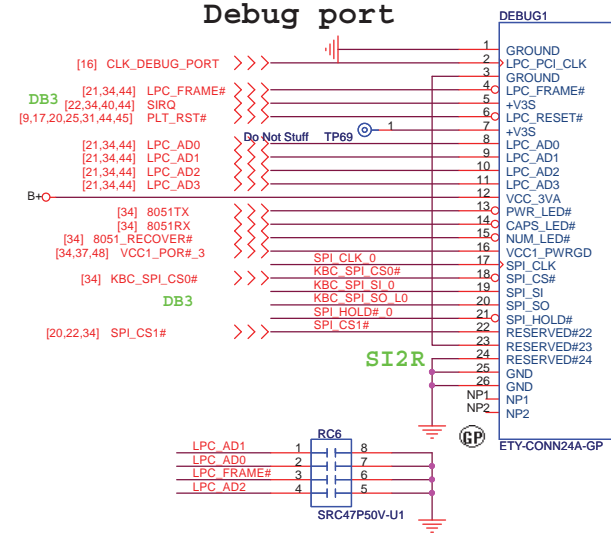


07/13 For KBC1091
reserve

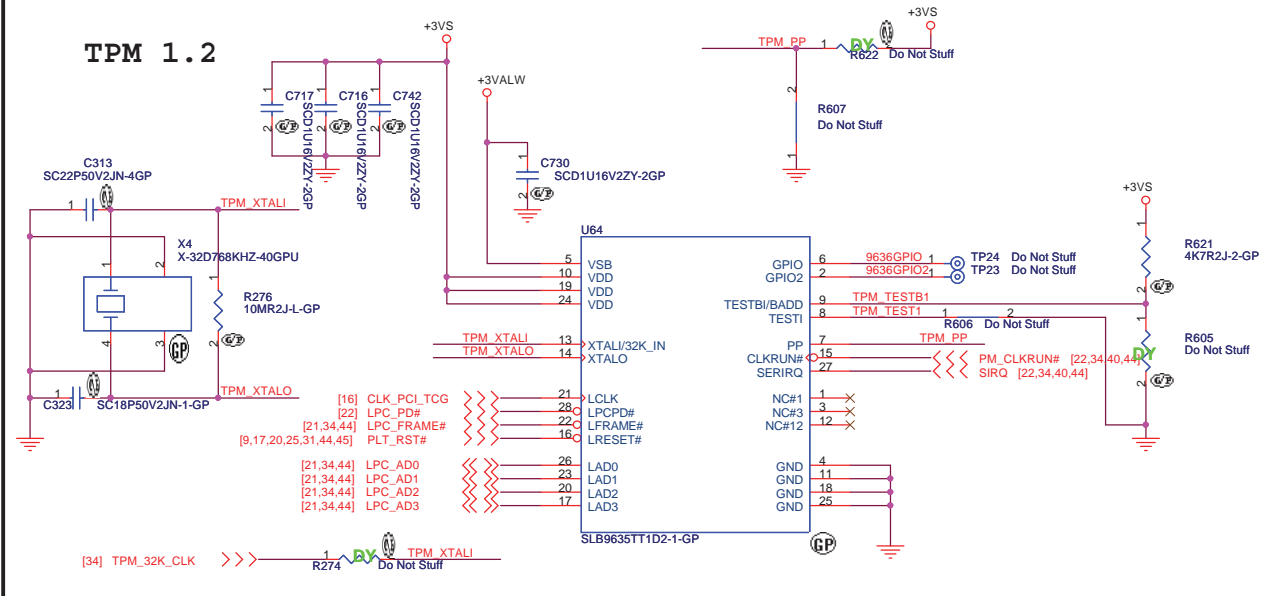
07/25 For KBC1091
reserve



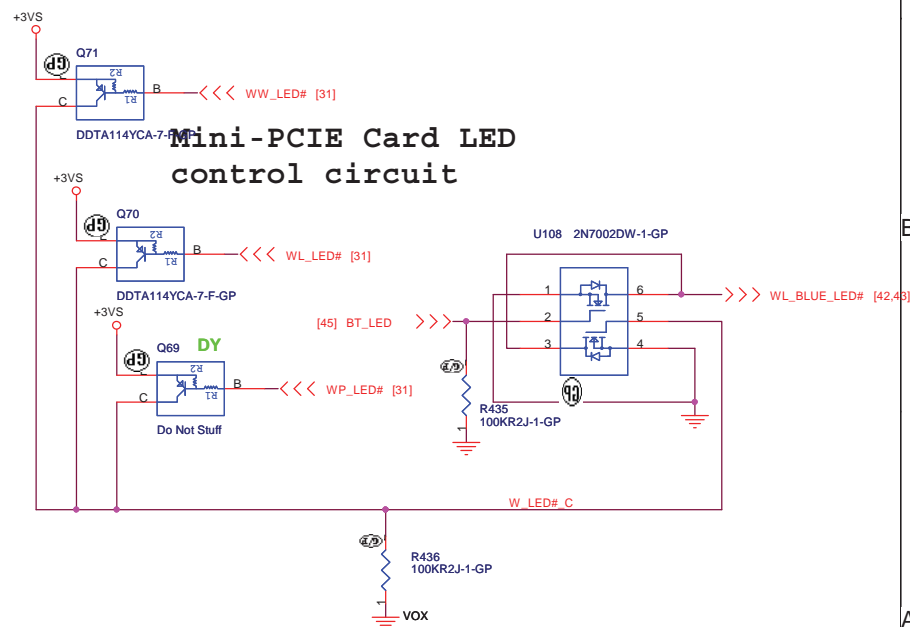
Debug port



TPM 1.2

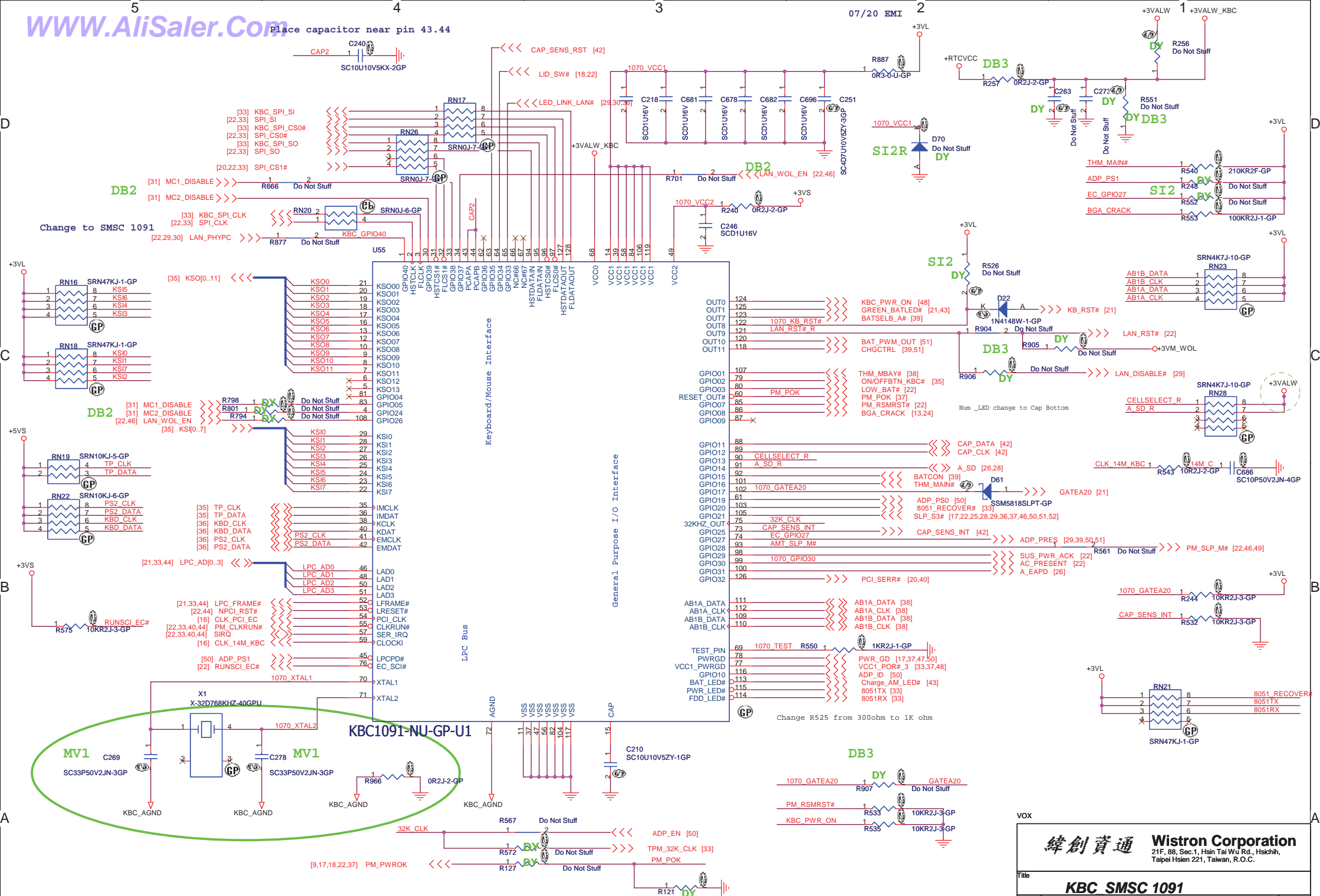


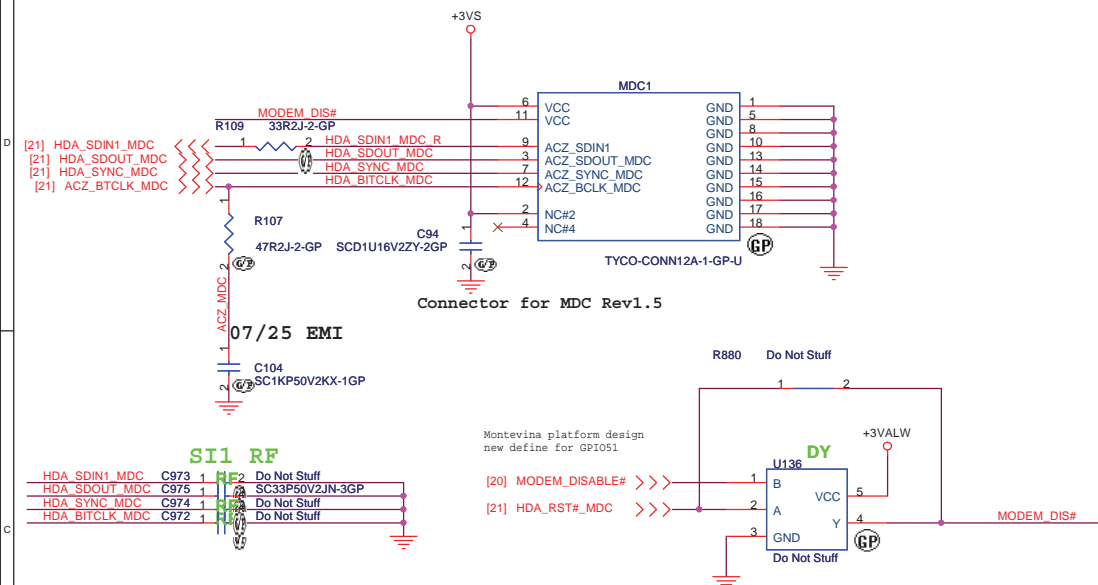
Mini-PCIE Card LED control circuit



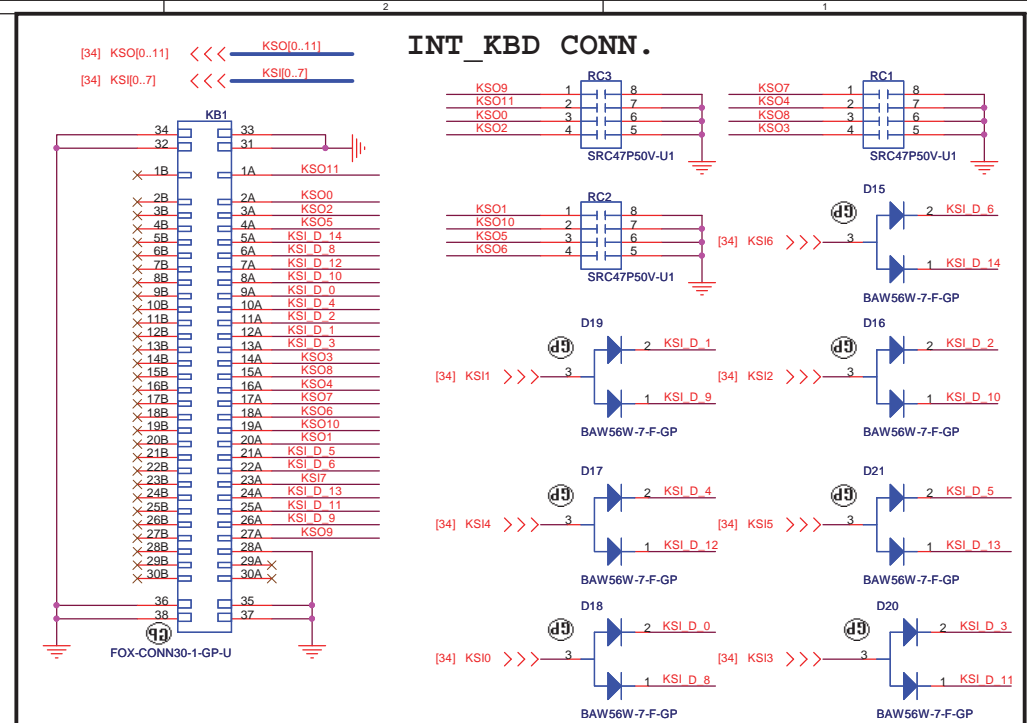
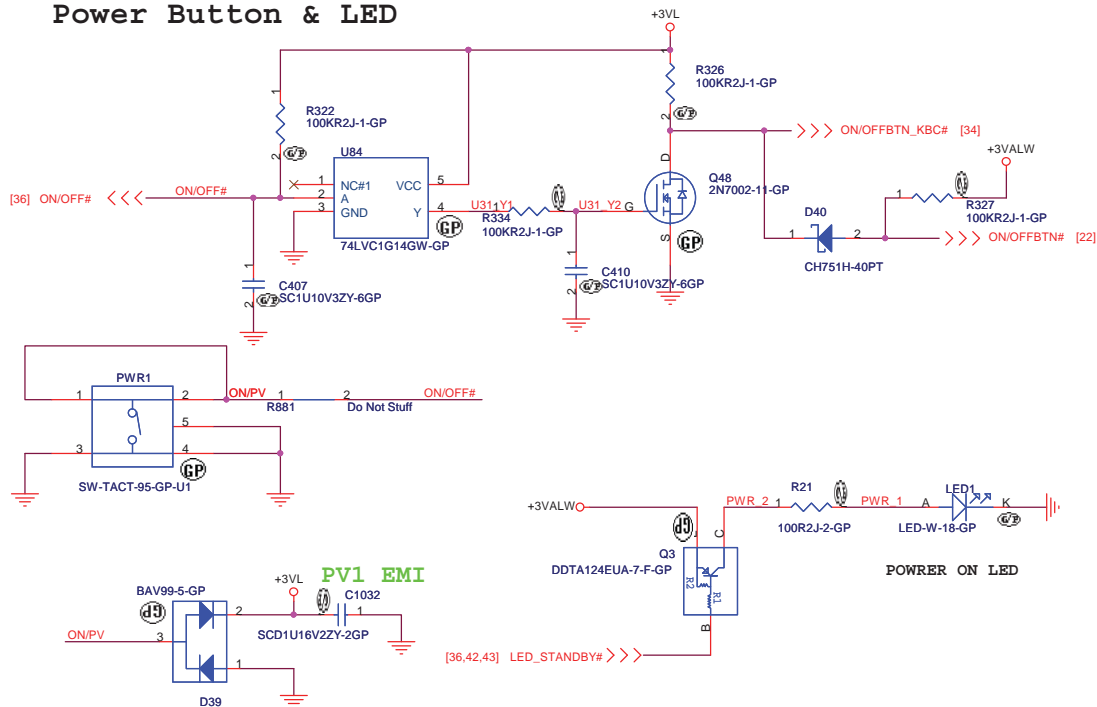
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TPM/BIOS/24 DEBUG PORT			
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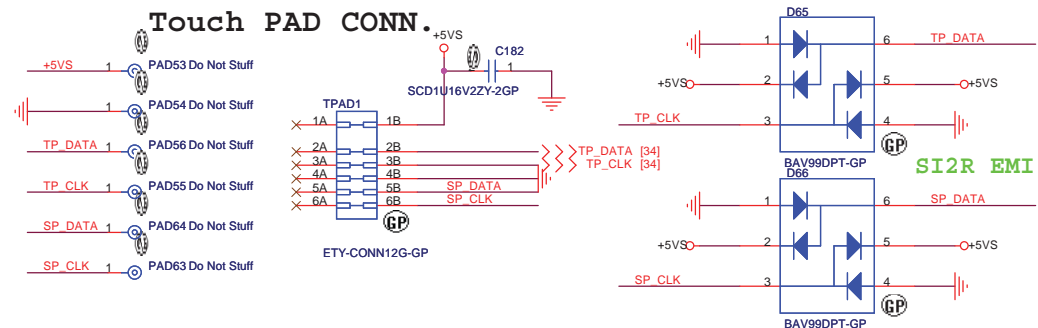




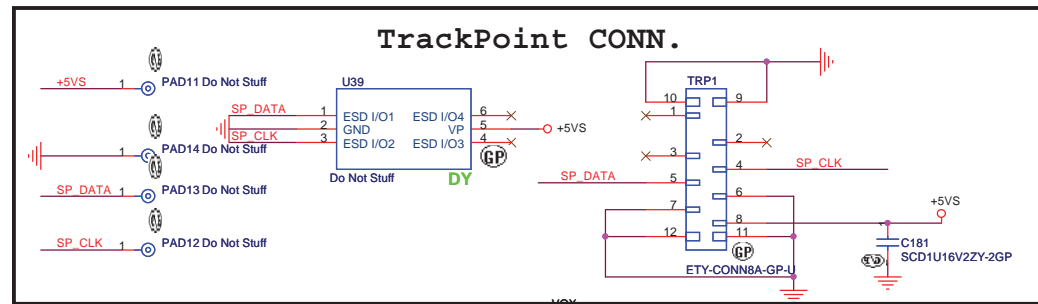
Power Button & LED

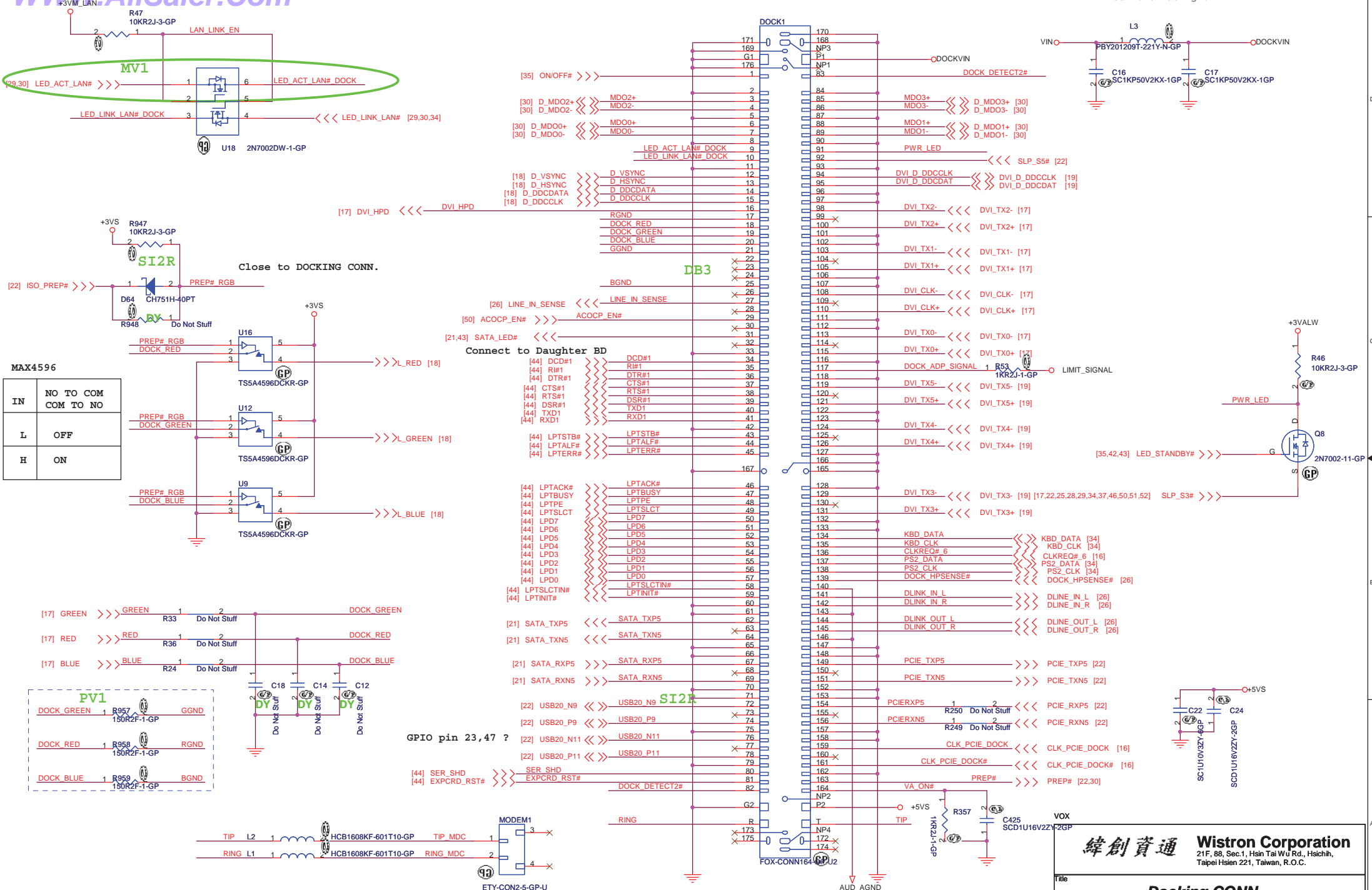


Touch PAD CONN.



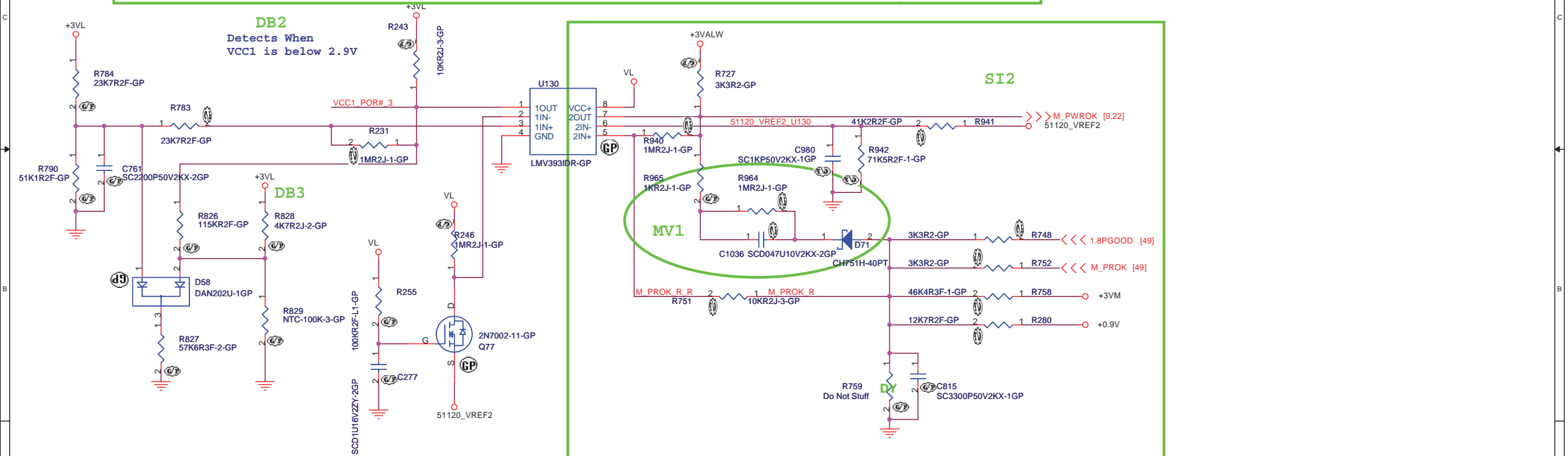
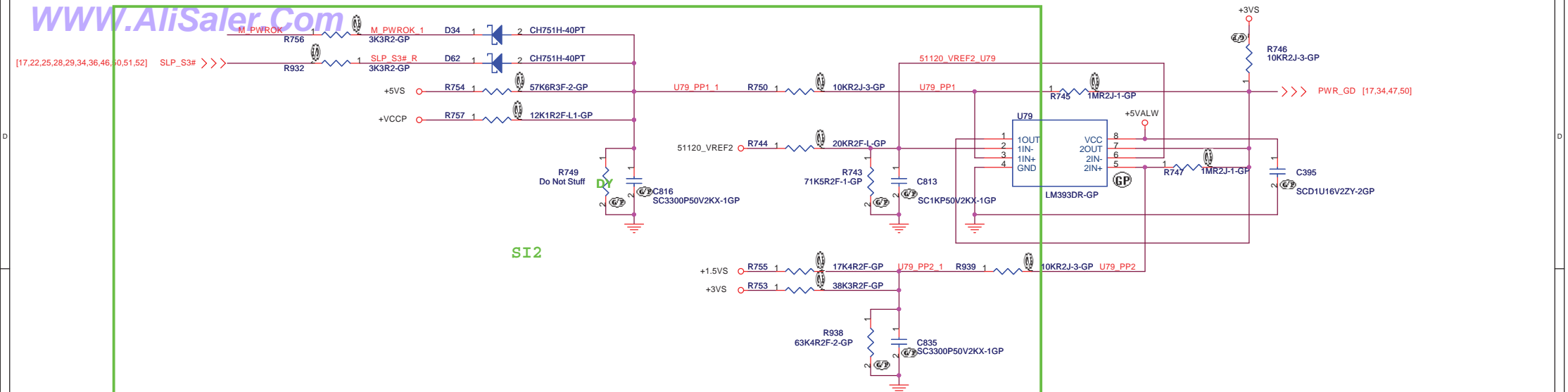
TrackPoint CONN.



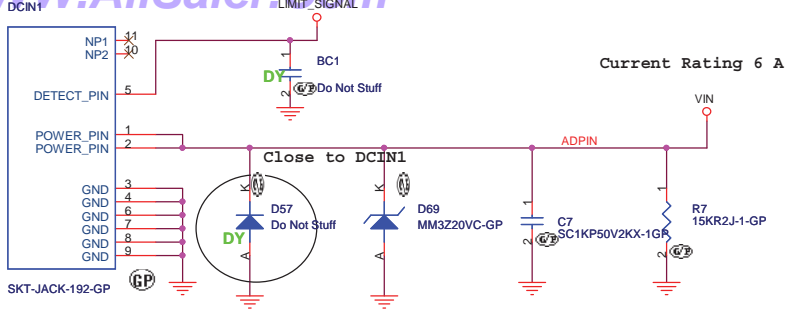


Layout Notes :
Place MODEM1 & BEAD near Docking connector

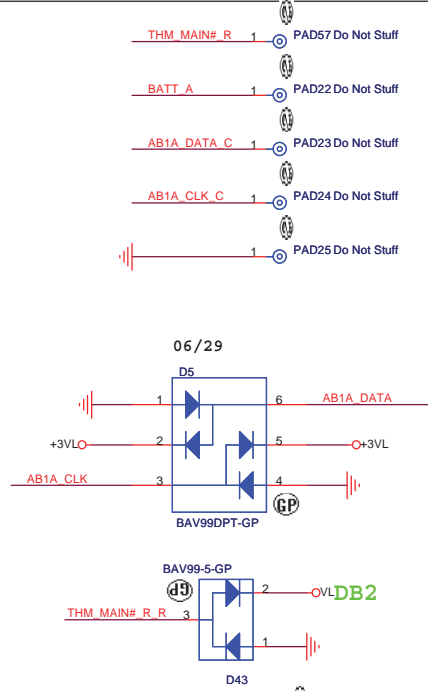
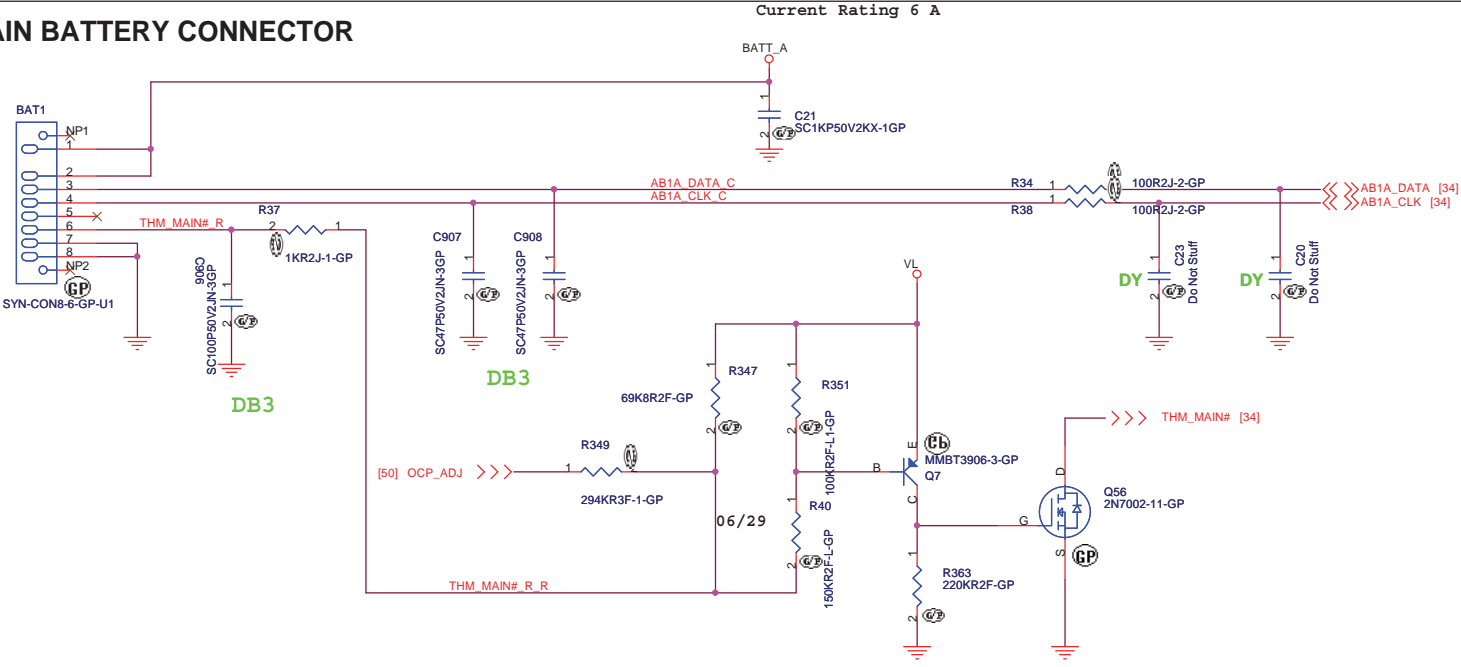
For TIP and Ring cut all layers



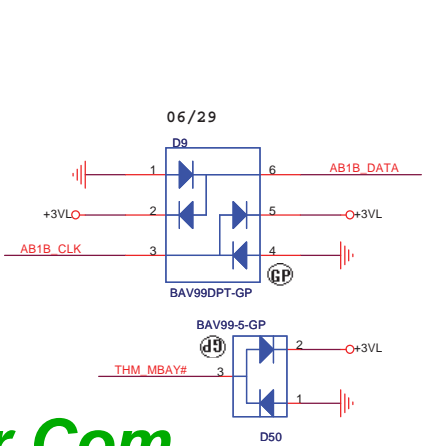
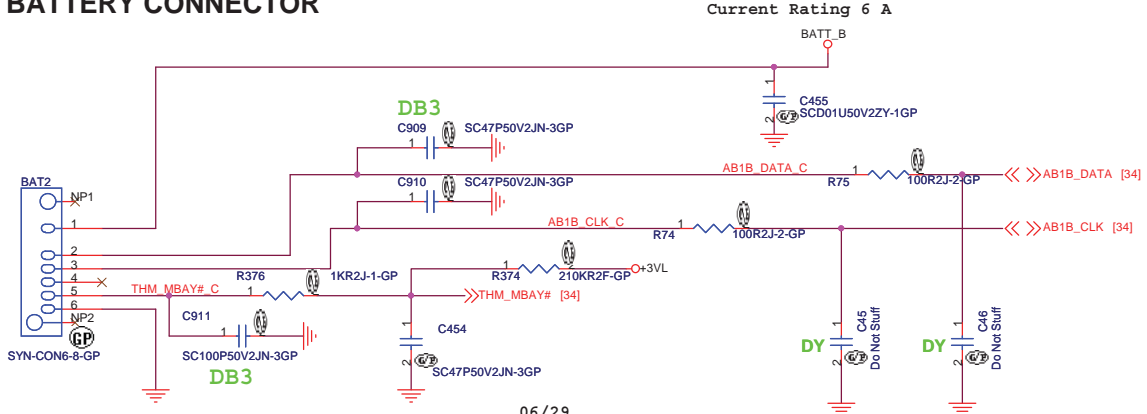
Adaptor in to generate DCBATOUT

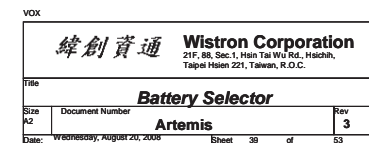


MAIN BATTERY CONNECTOR

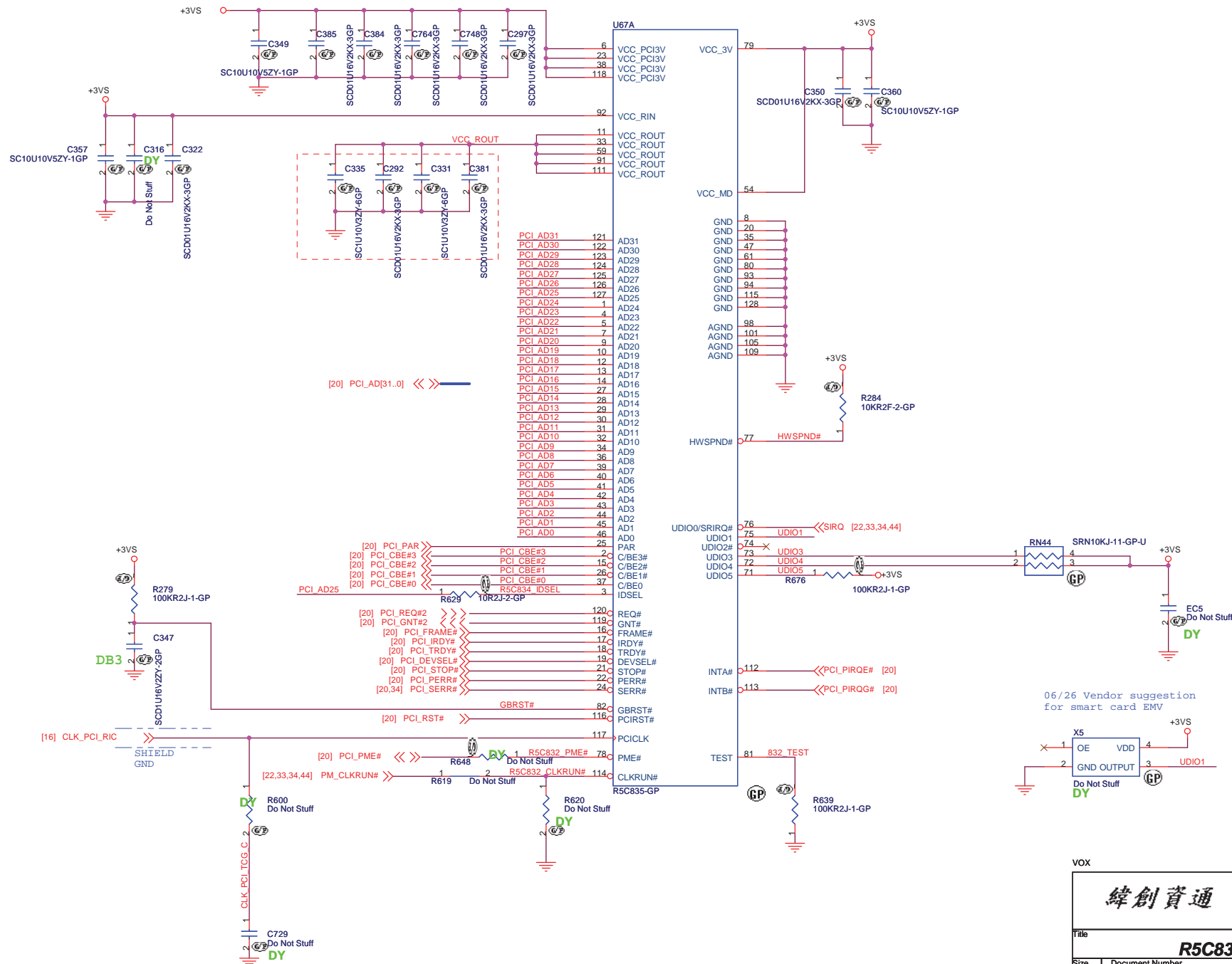


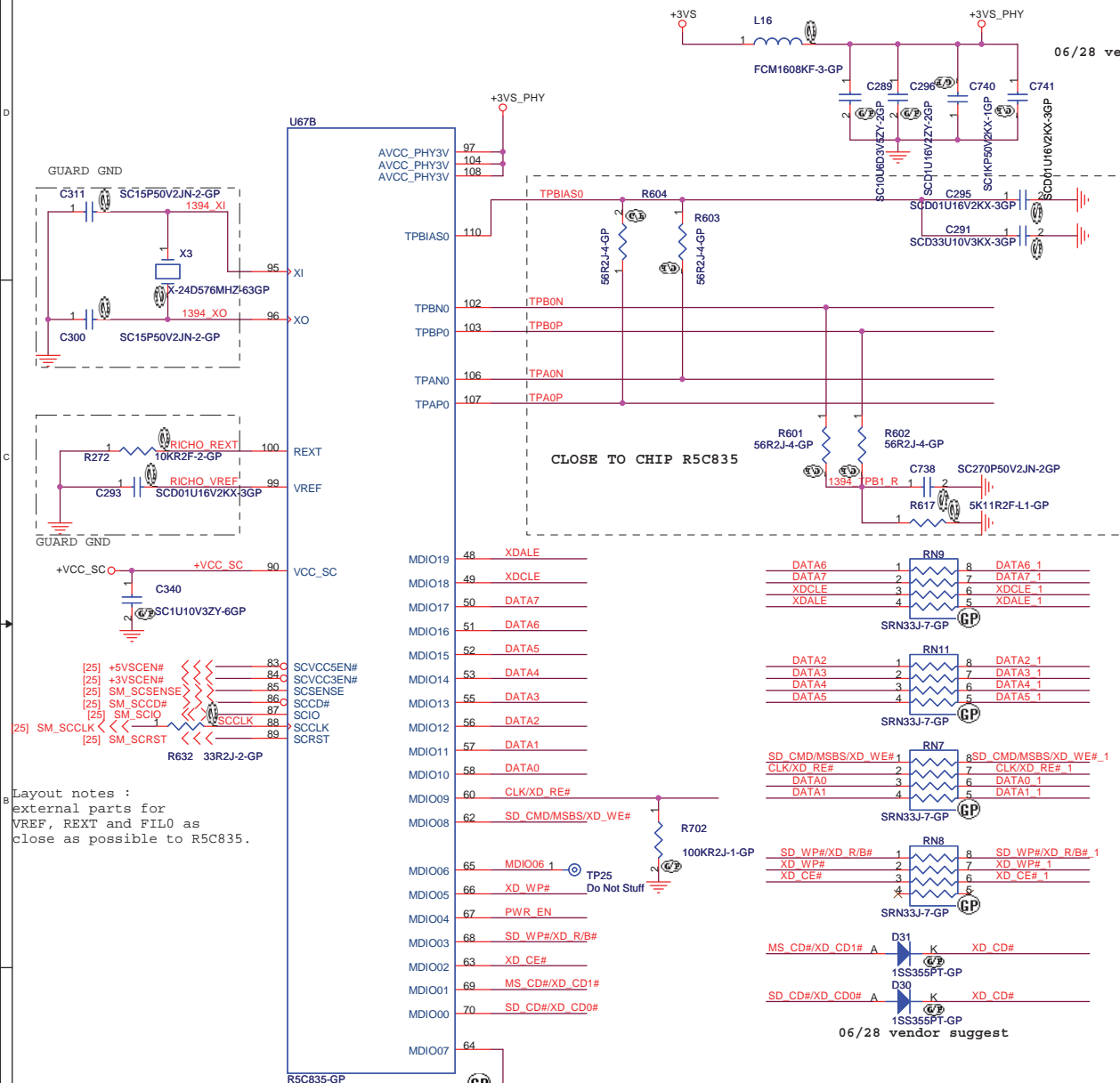
BAY BATTERY CONNECTOR



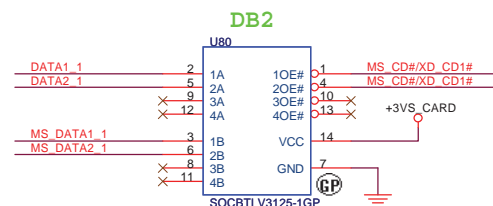


RICOH R5C835 (1 OF 2) PCI





Layout notes :
external parts for
VREF, REXT and FIL0 as
close as possible to R5C835.

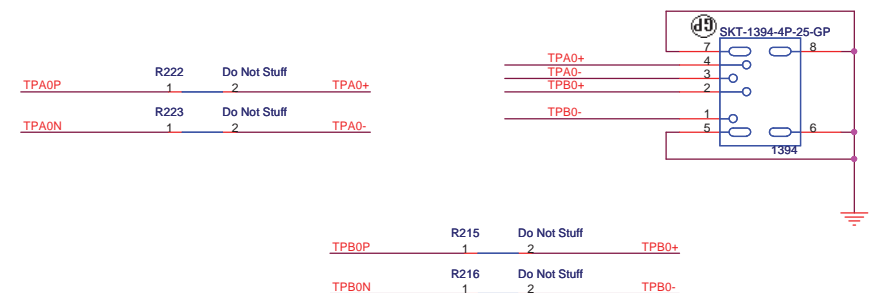


Layout notes :
1394

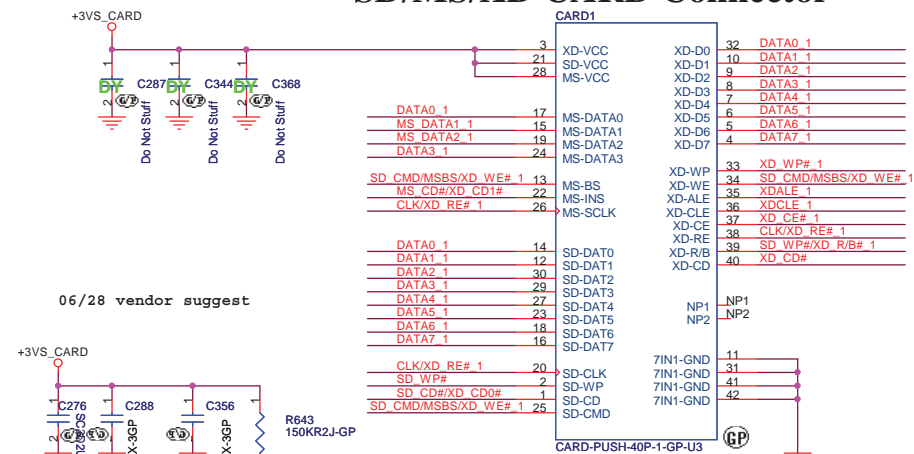
```

=====GND
-----TPB0-
-----TPB0+
=====GND
-----TPA0-
-----TPA0+
=====GND

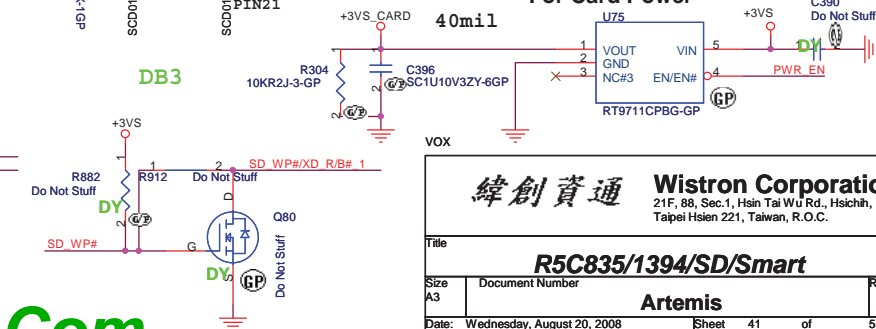
```



SD/MS/XD CARD Connector



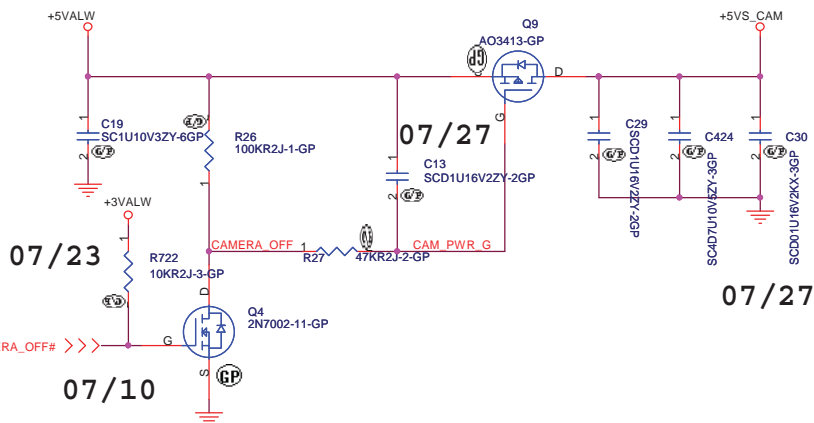
For Card Power



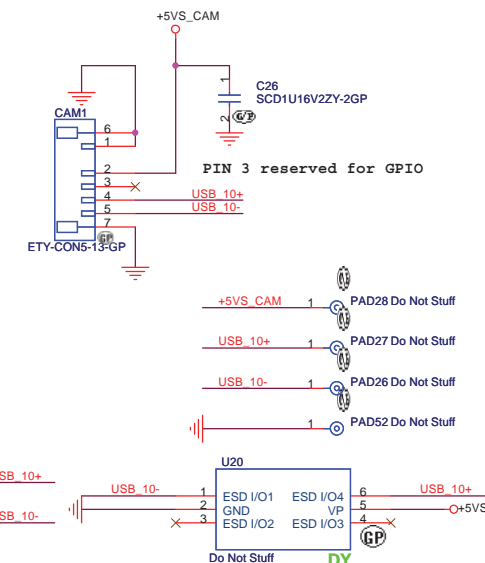
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
<i>R5C835/1394/SD/Smart</i>			
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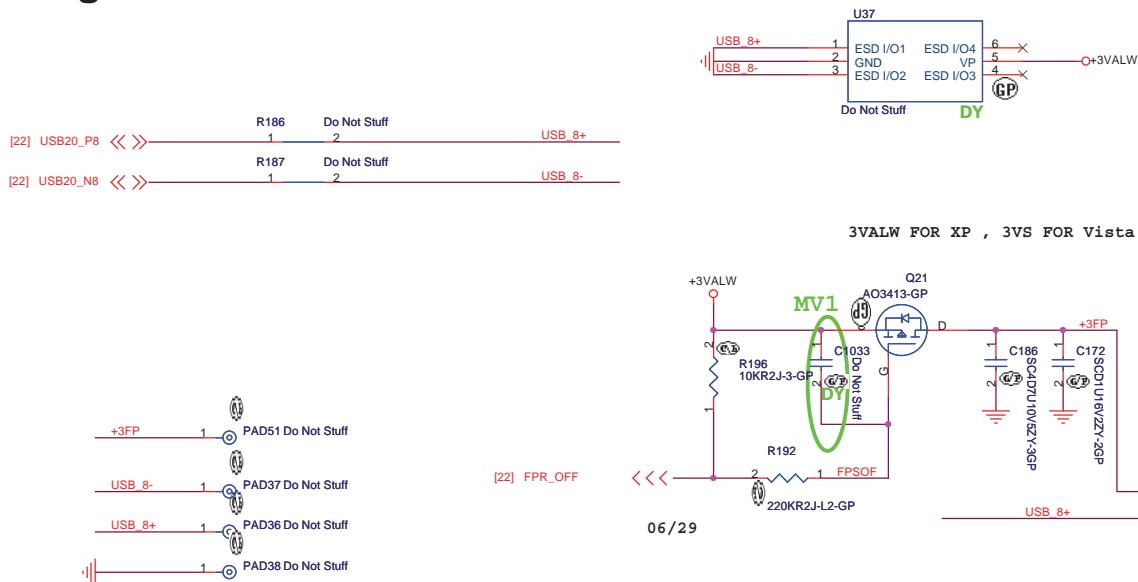
CAMERA



CAMERA Conn.



FingerPrint

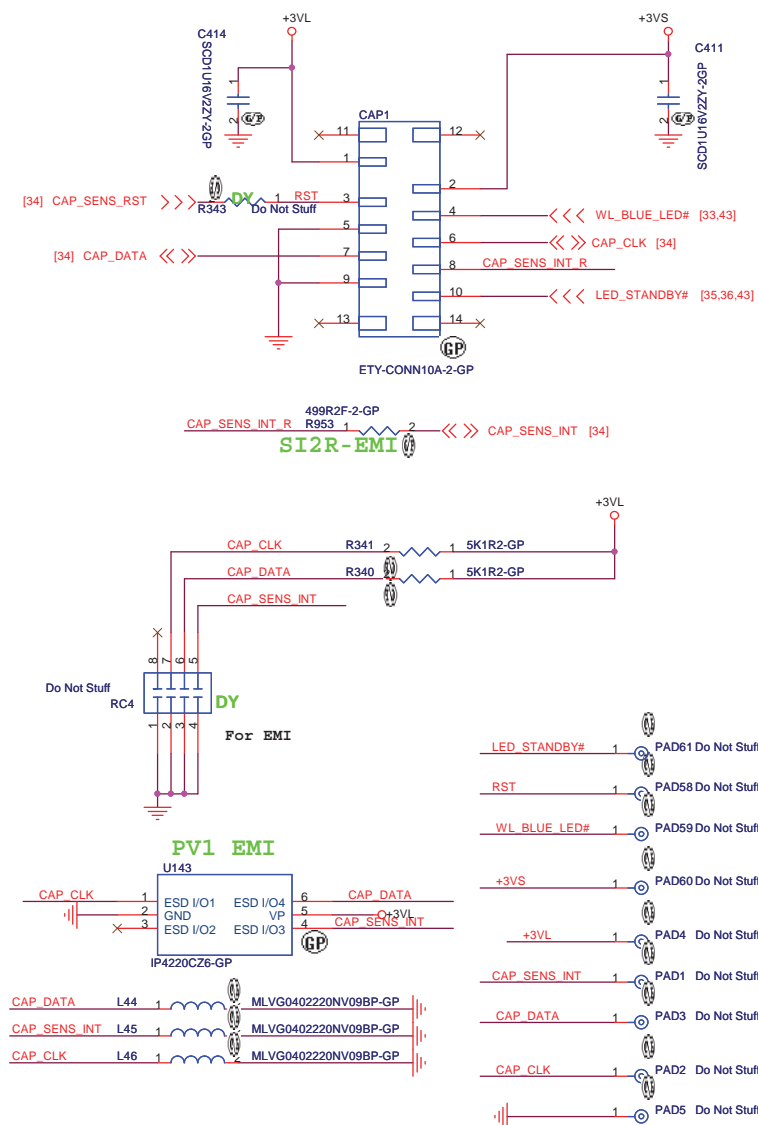


FingerPrint Conn.

SYSTEM CAPACITY BOARD

Vol up , Vol down , Mute , Presentation

PIN 3 reserved for GPIO



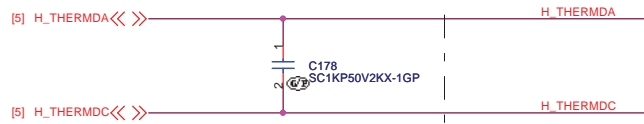
VOX

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Taipei Hsien 221, Taiwan, R.O.C.

Title			
Camera/W-COM			
Size A3	Document Number		Rev
	Artemis		3
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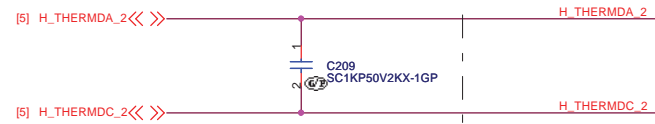
CPU TEMP:

H_THERMDA and H_THERMDC routing 10mil trace width and spacing. Locate Capacity near ADT7473.

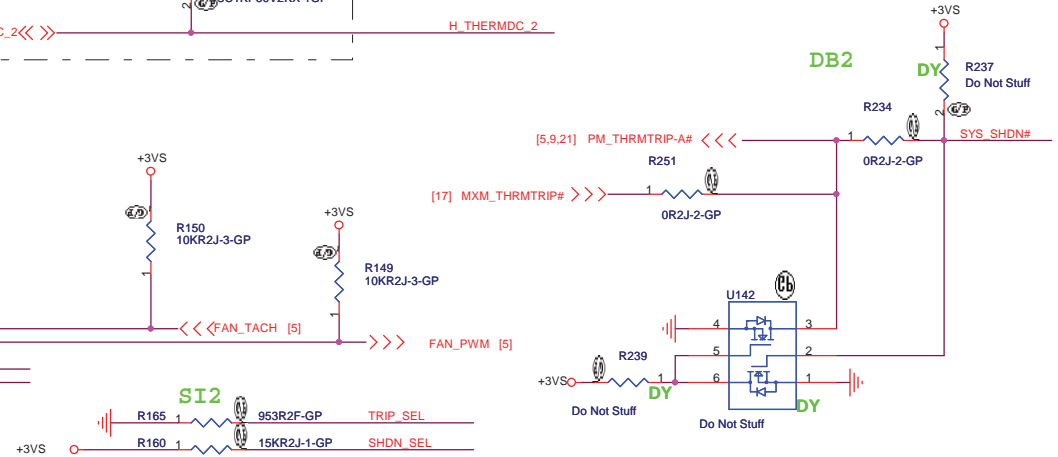
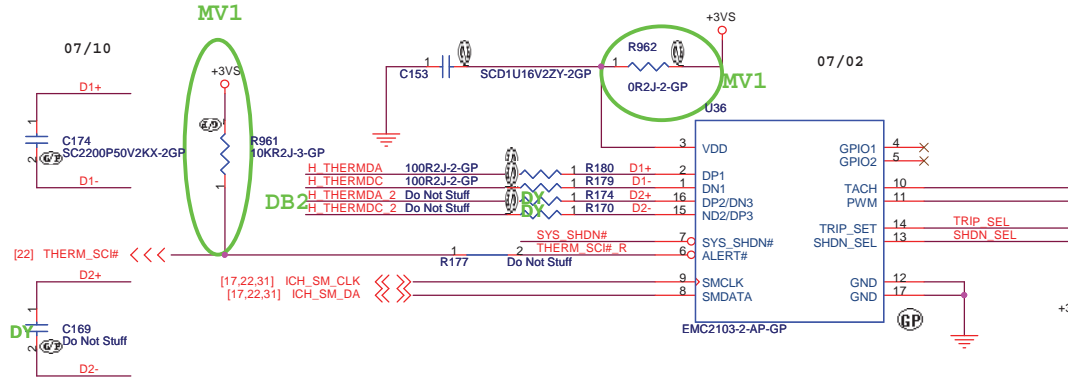


CPU TEMP:

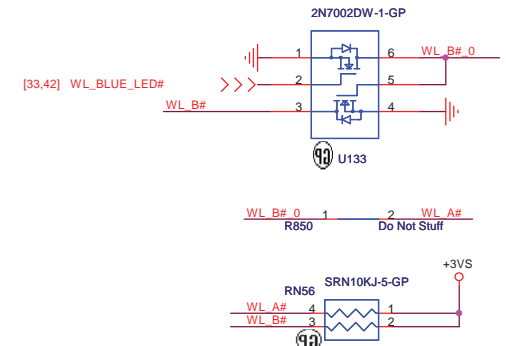
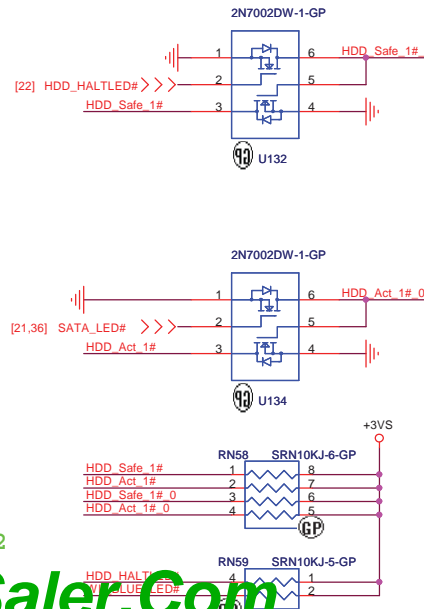
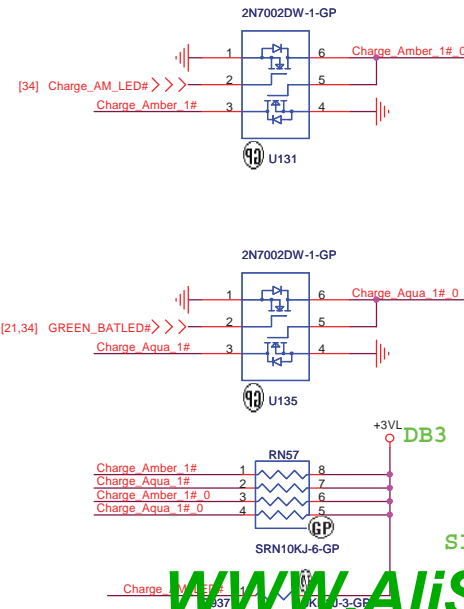
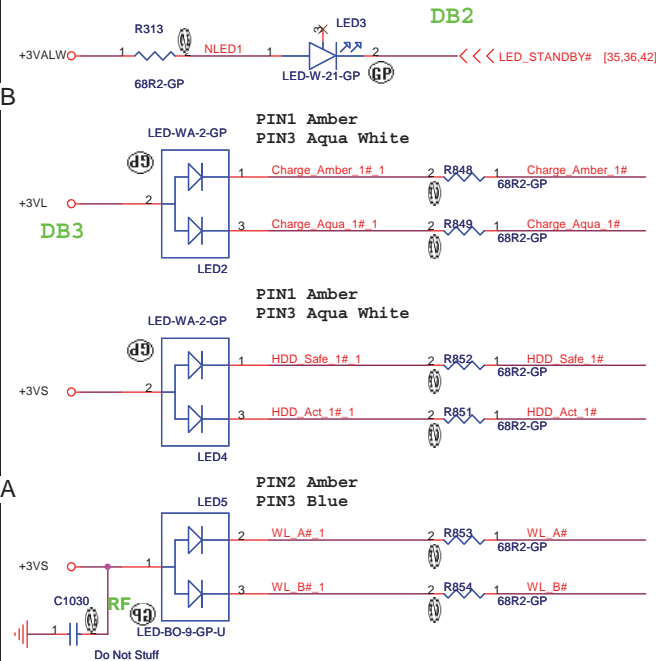
H_THERMDA and H_THERMDC routing 10mil trace width and spacing. Locate Capacity near ADT7473.



DB3 Change Power to 3VS for A009 issue



LED

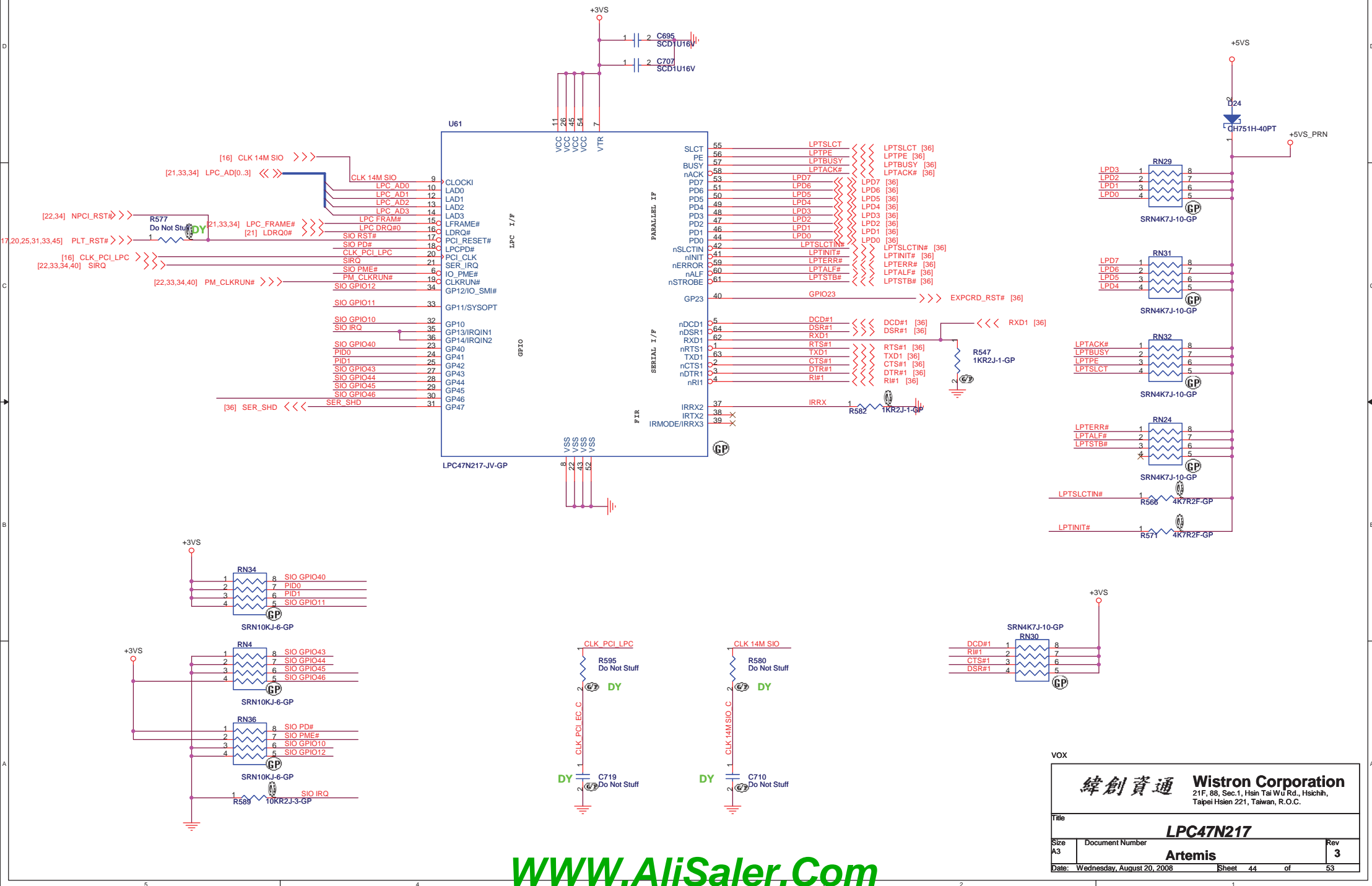


VOX

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Title			ADT7473 Thermal Sensor
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SIO

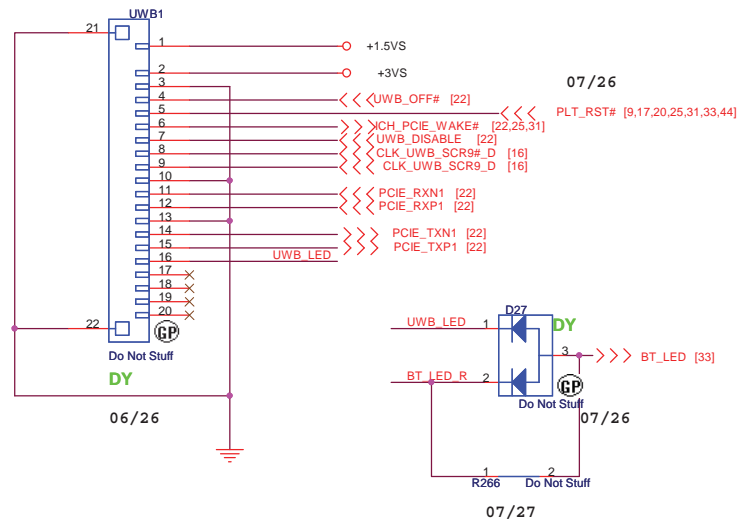


VOX

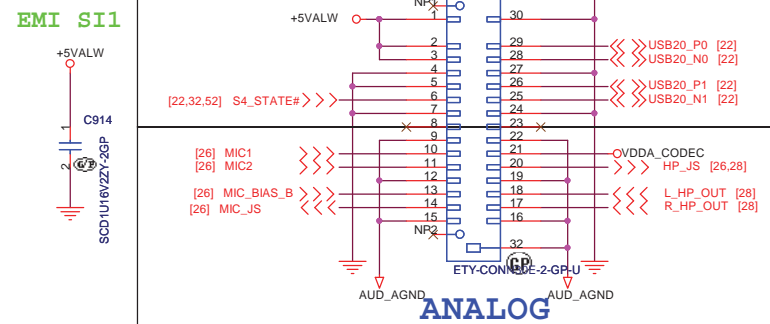
緯創資通 Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
 Taipei Hsien 221, Taiwan, R.O.C.

Title			
LPC47N217			
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A3		Artemis	
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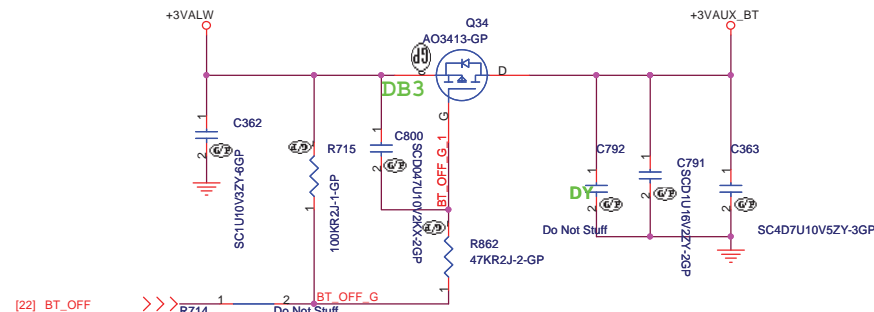
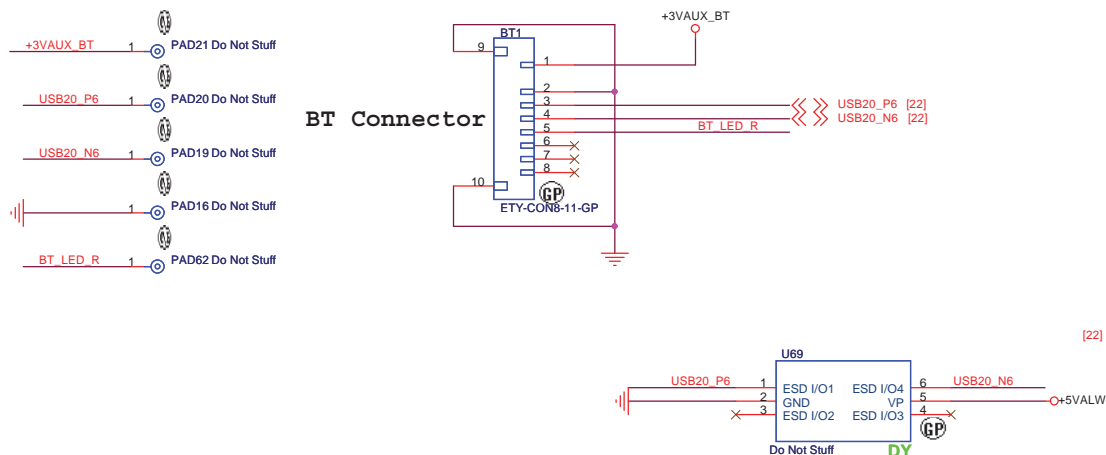
UWB Connector



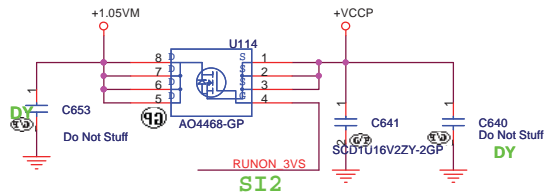
AUDIO Daughter Board Connector



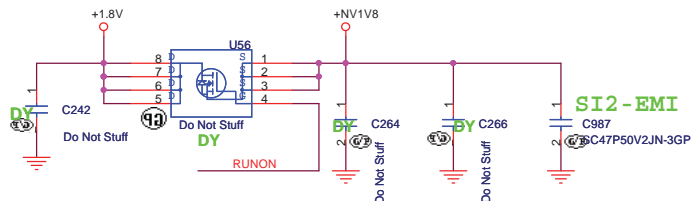
Bluetooth Connector



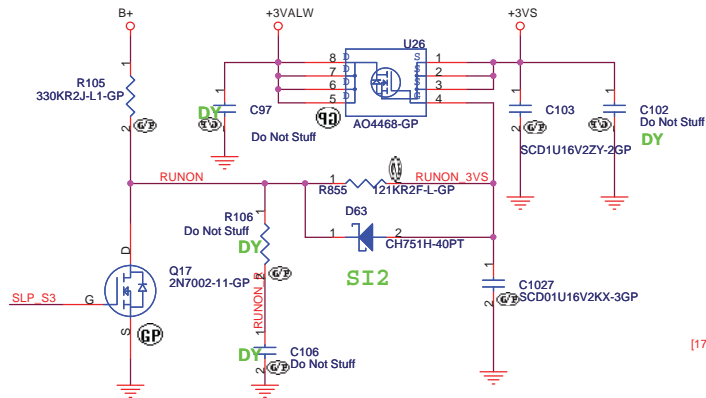
+1.05VM to +VCCP Transfer



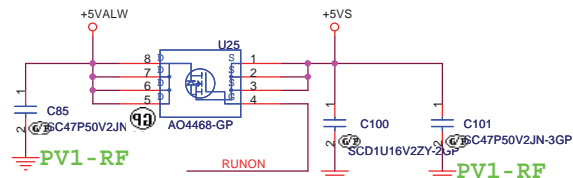
+1.8V to +1.8VS Transfer



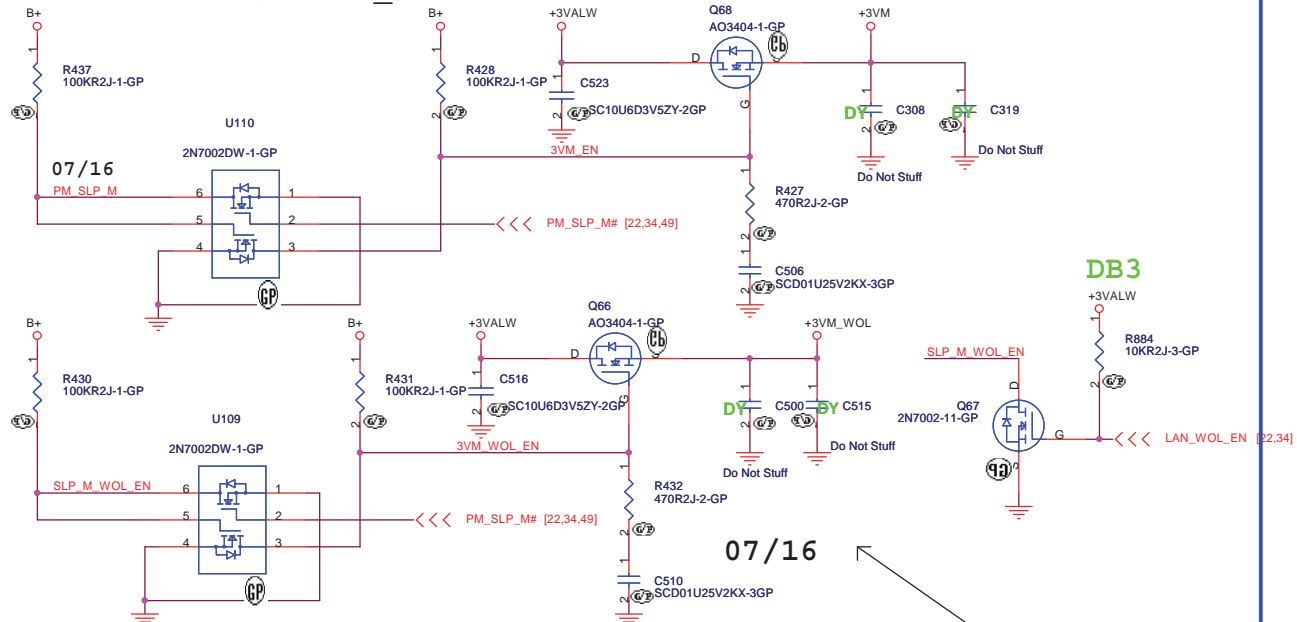
+3VALW to +3VS Transfer



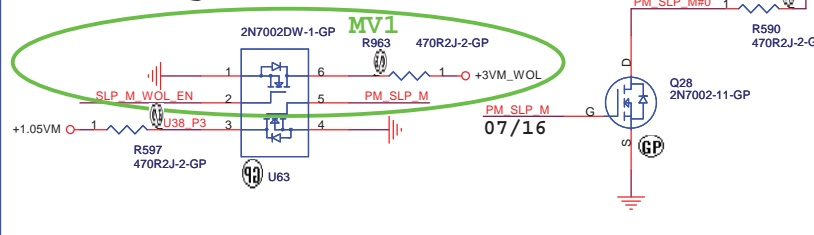
+5VALW to +5VS Transfer



+3VALW to +3VM / +3VM_WOL Transfer

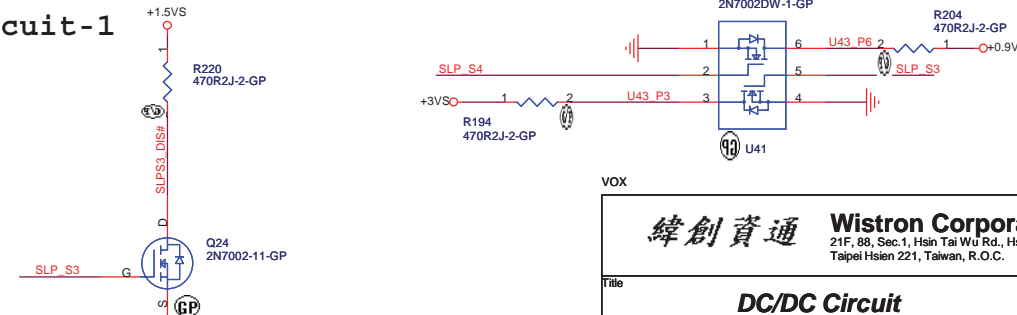


Discharge circuit-2 for V-M



PM_SLP_M#	LAN_WOL_EN	+3VM_WOL	+3VM	SYSTEM STATE
0	0	0V	0V	Mo ff / No WOL
0	1	3.3V	0V	Legacy WOL/ Mo ff
1	0	3.3V	3.3V	M1
1	1	3.3V	3.3V	M1

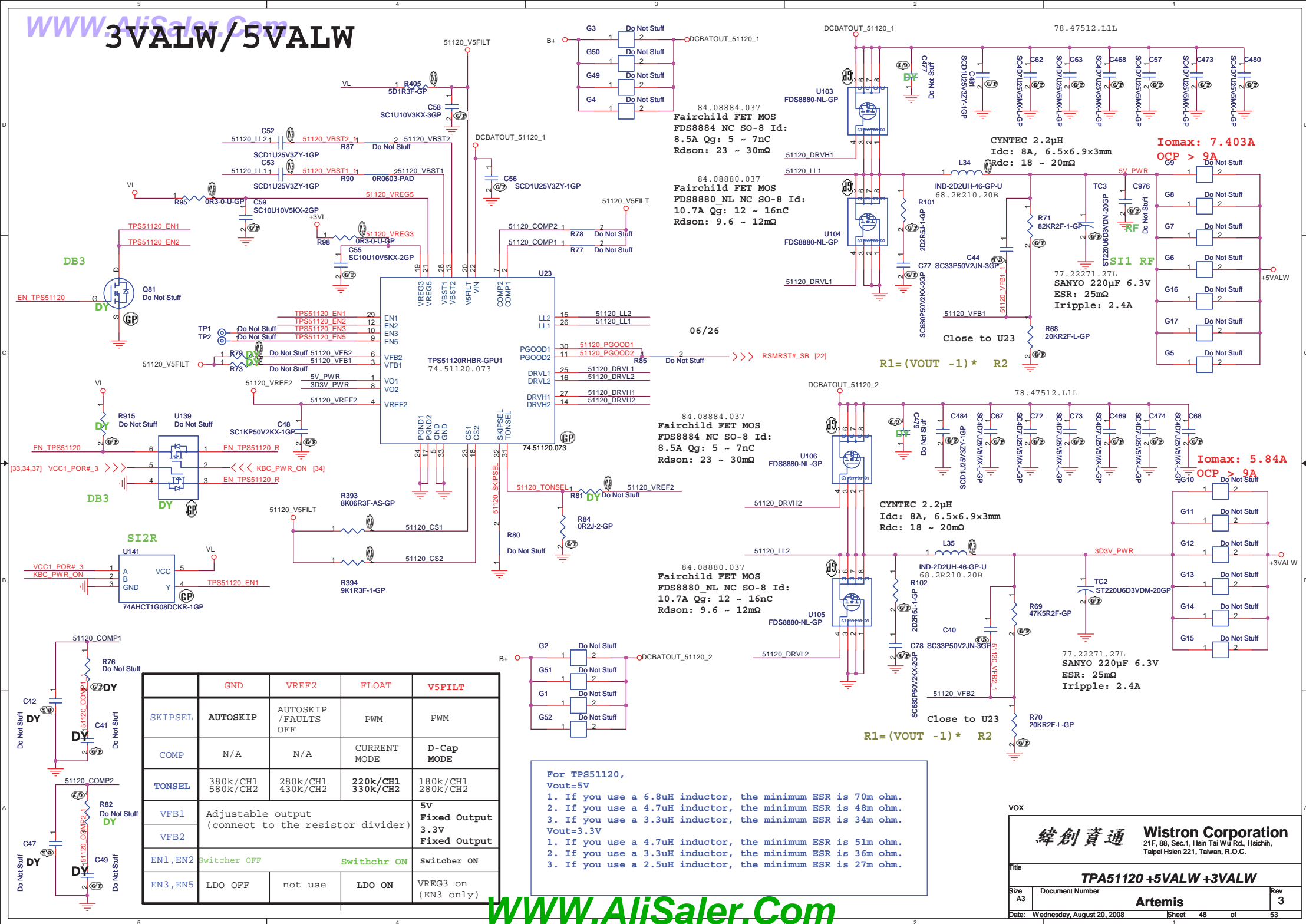
Discharge circuit-1



VOX

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DC/DC Circuit			
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	GND	VREF2	FLOAT	V5FILT
SKIPSEL	AUTOSKIP	AUTOSKIP / FAULTS OFF	PWM	PWM
COMP	N/A	N/A	CURRENT MODE	D-Cap MODE
TONSEL	380k/CH1 580k/CH2	280k/CH1 430k/CH2	220k/CH1 330k/CH2	180k/CH1 280k/CH2
VFB1	Adjustable output (connect to the resistor divider)			5V Fixed Output 3.3V Fixed Output
VFB2				
EN1, EN2	Switcher OFF	Switchchr ON	Switcher ON	
EN3, EN5	LDO OFF	not use	LDO ON	VREG3 on (EN3 only)

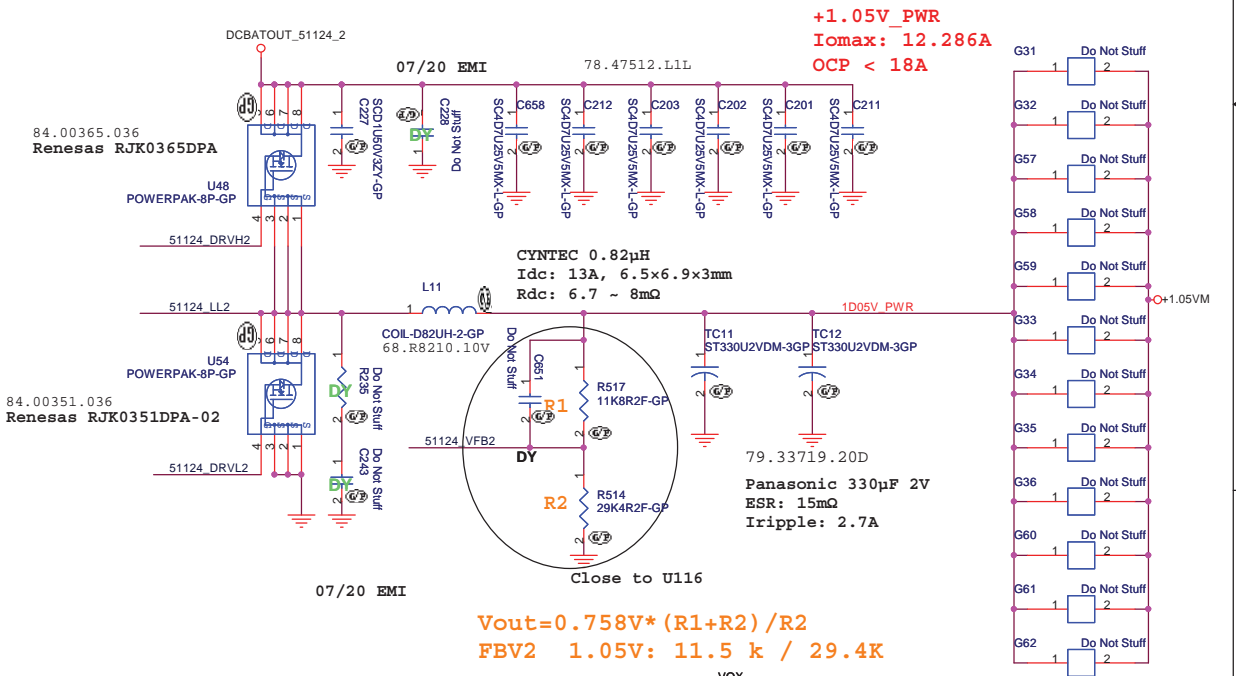
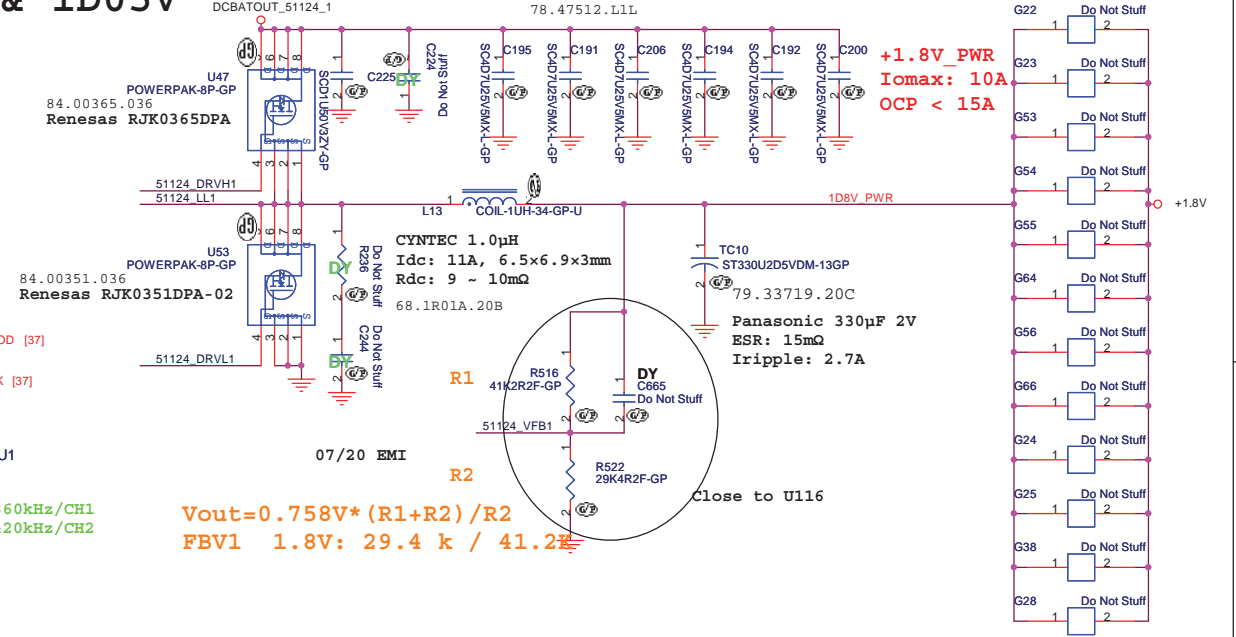
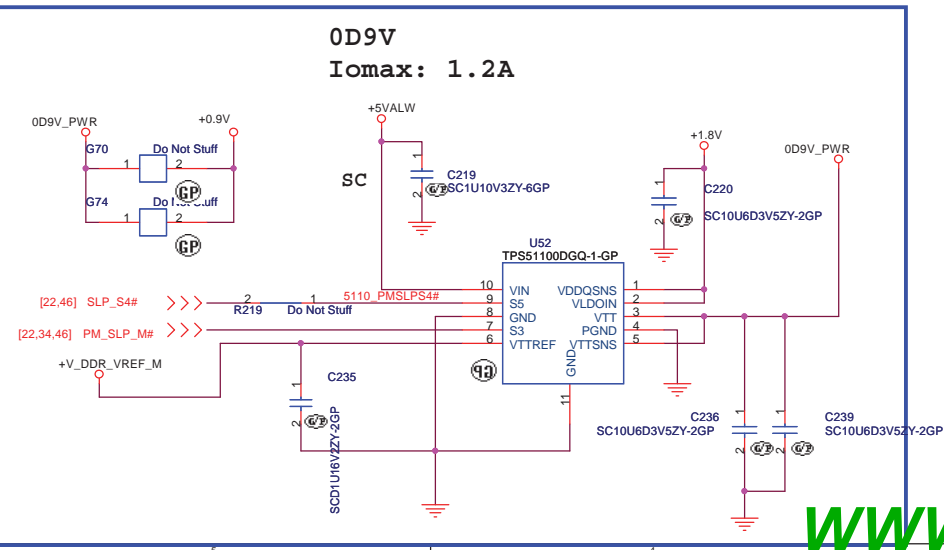
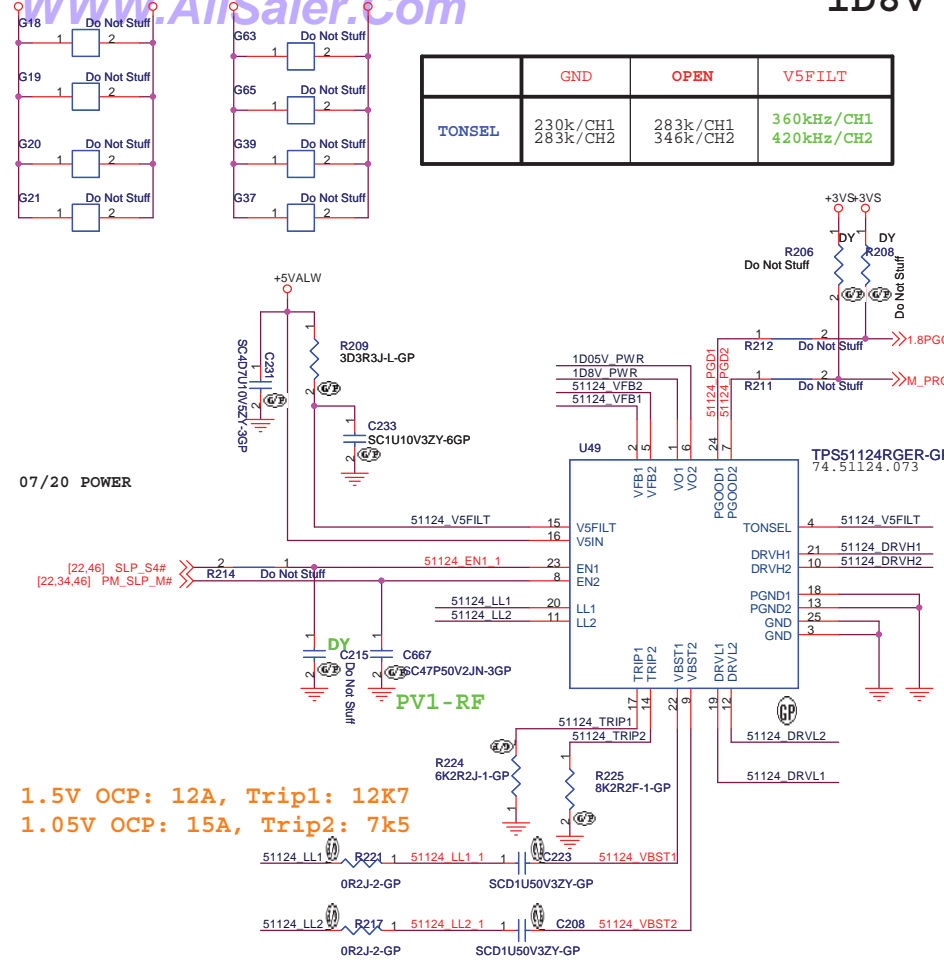
For TPS51120,
Vout=5V


1. If you use a 6.8uH inductor, the minimum ESR is 70m ohm.
2. If you use a 4.7uH inductor, the minimum ESR is 48m ohm.
3. If you use a 3.3uH inductor, the minimum ESR is 34m ohm.

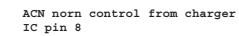
Vout=3.3V

1. If you use a 4.7uH inductor, the minimum ESR is 51m ohm.
2. If you use a 3.3uH inductor, the minimum ESR is 36m ohm.
3. If you use a 2.5uH inductor, the minimum ESR is 27m ohm.

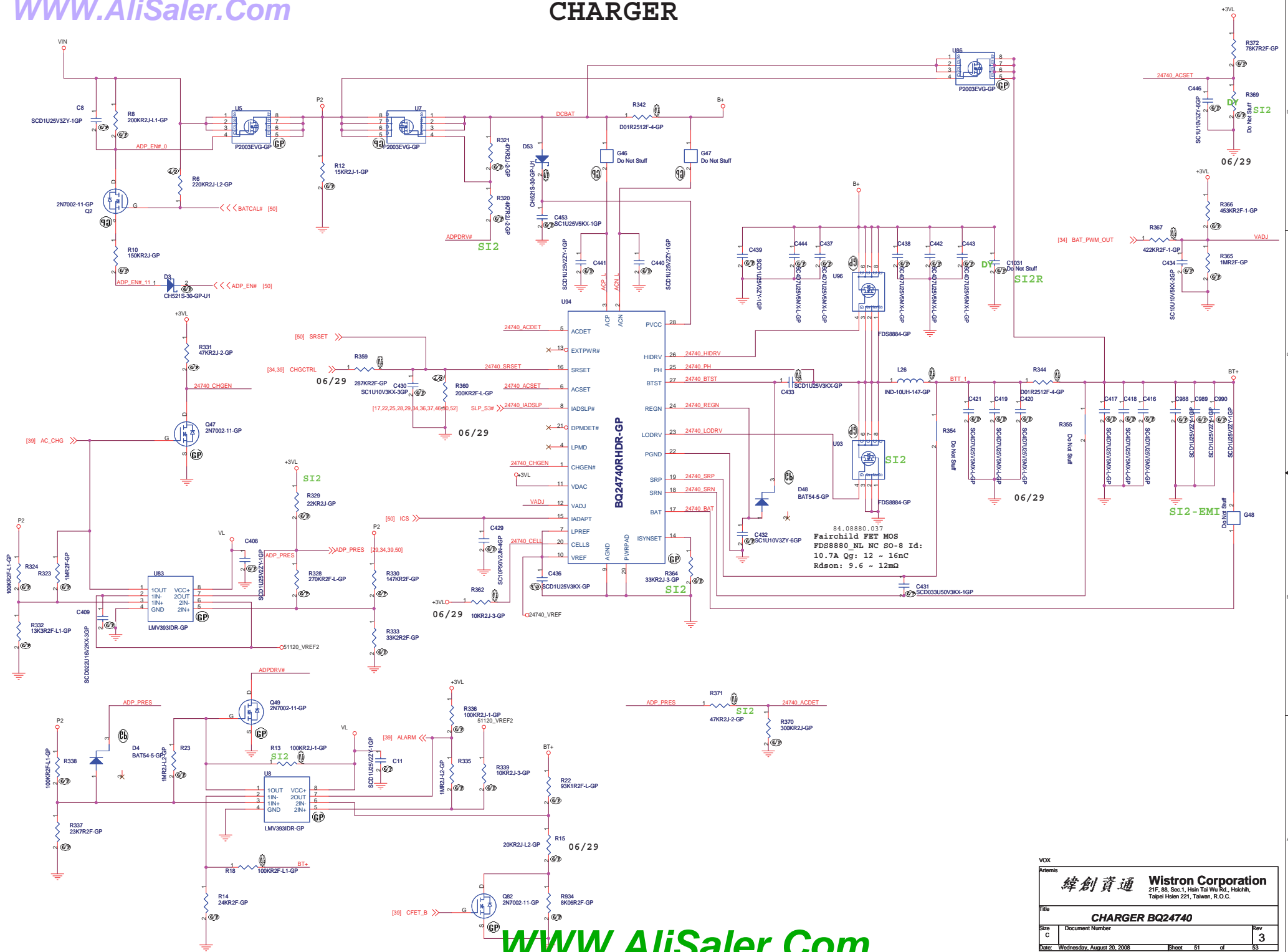
	GND	OPEN	V5FILT
TONSEL	230k/CH1 283k/CH2	283k/CH1 346k/CH2	360kHz/CH1 420kHz/CH2



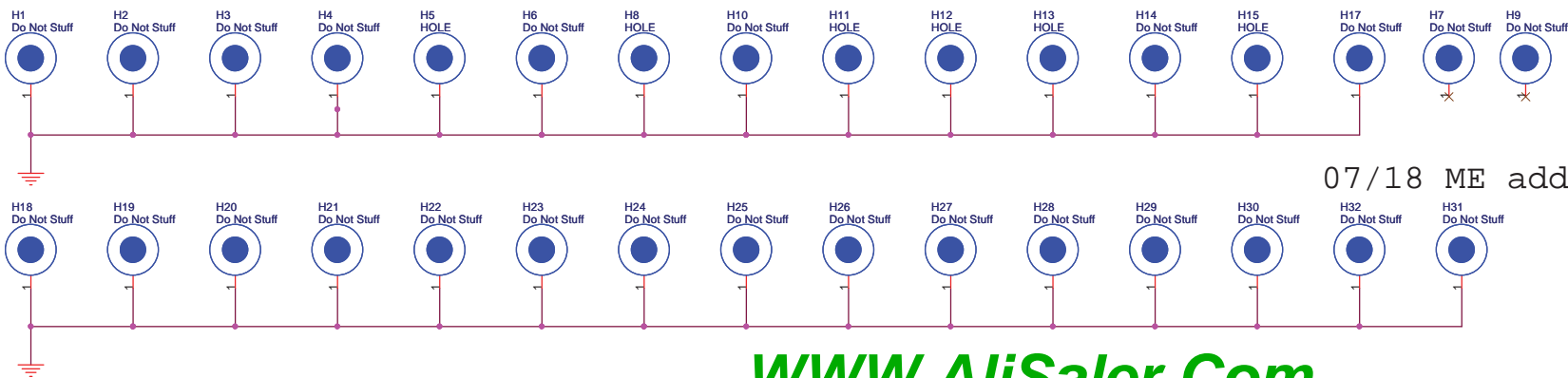
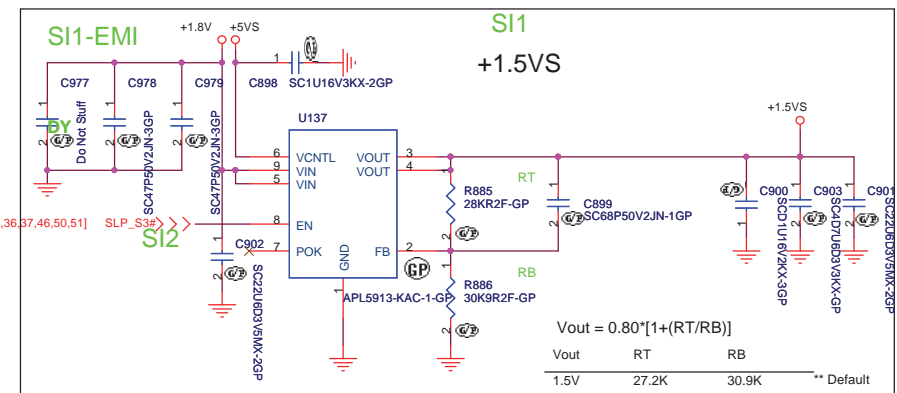
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TPS51124 1D5V/1D05V			
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